Compiling First-Order Functions to Session-Typed Parallel Code

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Abstract

Building correct and efficient message-passing parallel programs still poses many challenges. The incorrect use of message-passing constructs can introduce deadlocks, and a bad task decomposition will not achieve good speedups. Current approaches focus either on correctness or efficiency, but limited work has been done on ensuring both. In this paper, we propose a new parallel programming framework, PAlg, which is a first-order language with *participant annotations* that ensures *deadlock-freedom by construction*. PAlg programs are coupled with an abstraction of their communication structure, a *global type* from the theory of *multiparty session types* (MPST). This global type serves as an output for the programmer to assess the efficiency of their achieved parallelisation. PAlg is implemented as an EDSL in Haskell, from which we: 1. compile to low-level message-passing C code; 2. compile to sequential C code, or interpret as sequential Haskell functions; and, 3. infer the communication protocol followed by the compiled message-passing program. We use the properties of global types to perform message reordering optimisations to the compiled C code. We prove the ***extensional equivalence*** of the compiled code, as well as ***protocol compliance***. We achieve linear speedups on a shared-memory 12-core machine, and a speedup of 16 on a 2-node, 24-core NUMA.

Keywords multiparty session types, parallelism, arrows

1 Introduction

Structured parallel programming is a technique for parallel programming that requires the use of high-level parallel constructs, rather than low-level send/receive operations [52; 62]. A popular approach to structured parallelism is the use of *algorithmic skeletons* [20; 36], i.e. higher-order functions that implement common patterns of parallelism. Programming in terms of high-level constructs rather than low-level send/receive operations is a successful way to avoid common concurrency bugs *by construction* [38]. One limitation of structured parallelism is that it restricts programmers to use a set of fixed, predefined parallel constructs. This is problematic if a function does not match one of the available parallel constructs, or if a program needs to be ported to an architecture where some of the skeletons have not been implemented. Unlike previous structured parallelism approaches, we do not require the existence of an underlying library or implementation of common patterns of parallelism.

In this paper, we propose a structured parallel programming framework whose front-end language is a first-order language based on the *algebra of programming* [2; 3]. The algebra of programming is a mathematical framework that codifies the basic laws of algorithmics, and it has been successfully applied to e.g. *program calculation* techniques [4], *datatype-generic programming* [35], and *parallel computing* [66]. Our framework produces message-passing parallel code from program specifications written in the front-end language. The programmer controls how the program is parallelised by *annotating* the code with *participant identifiers*. To make sure that the achieved parallelisation is satisfactory, we produce as an output a formal description of the *communication protocol* achieved by a particular parallelisation. This formal description is a *global type*, introduced by Honda et al. [42] in the theory of *Multiparty Session Types* (MPST). We prove that the parallelisation, and any optimisation performed to the low-level code respects the inferred protocol. The properties of global types justify the message reordering done by our back-end. In particular, we permute send and receive operations whenever sending does not depend on the values received. This is called *asynchronous optimisation* [57], and removes unnecessary synchronisation, while remaining communication-safe.

1.1 Overview

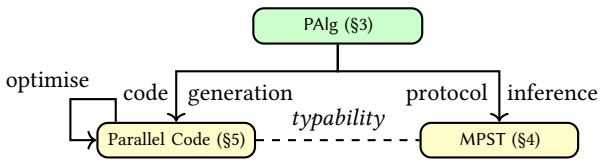

Figure 1. Overview

Our framework has three layers: (1) Parallel Algebraic Language (PAlg), a point-free first-order language with *participant annotations*, which describe which process is in charge of executing which part of the computation; (2) Message

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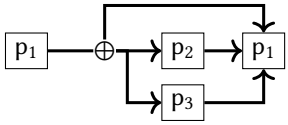
Passing Monad (Mp), a monadic language that represents low-level message-passing parallel code, from which we generate parallel C code; and (3) *global types* (from MPST), a formal description of the protocol followed by the output Mp code. Fig. 1 shows how these layers interact. PAlg, highlighted in green, is the input to our framework; and Mp and global types (MPST), highlighted in yellow, are the outputs. We prove that the generated code behaves as prescribed by the global type, and any low-level optimisation performed on the generated code must respect the protocol. As an example, we show below a parallel mergesort. mergesort.

```
1  msort :: (CVal a, CAlg f) => Int -> f [a] [a]
2  msort n = fix n $ \ms x -> vlet (vsize x) $ \sz ->
3    if sz <= 1 then x
4    else vlet (sz / 2) $ \sz2 ->
5         vlet (par ms $ vtake sz2 x) $ \xl ->
6         vlet (par ms $ vdrop sz2 x) $ \xr ->
7         app merge $ pair (sz, pair (xl, xr))
```

The return type of `msort`, `f [a] [a]`, is the type of first-order programs that take lists of *values* `[a]`, and return `[a]`. Constraint `CAlg` restricts the kind of operations that are allowed in the function definition. The integer parameter to function `fix` is used for rewriting the input programs, limiting the depth of *recursion unrolling*. `par` is used to *annotate* the functions that we want to run at different processes, and function `app` is used to run functions at the same participant as their inputs. In case this input comes from different participants, first all values are gathered at any of them, and then the function is applied. We can instantiate `f` either as a sequential program, as a parallel program, or as an MPST protocol. We prove that the sequential program, and output parallel programs are *extensionally equal*, and that the output parallel program *complies* with the inferred protocol. For example, interpreting `msort 1` as a parallel program produces C code that is extensionally equal to its sequential interpretation, and behaves as the following protocol:



This is a depth 1 divide-and-conquer, where p_1 divides the task, sends the sub-tasks to p_2 and p_3, and combines the results. If the input is small, p_1 produces the result directly.

Our prototype implementation is a *tagless-final* encoding [9] in Haskell of a point-free language. Constraint `CAlg` is a first-order form of *arrows* [45; 61], with a syntactic sugar layer that allows us to write code closer to (point-wise) idiomatic Haskell. The remainder of the paper focuses on the language underlying `CAlg`.

Why Multiparty Session Types There are both practical and theoretical advantages. On the theoretical side, the theory of multiparty session types ensures *deadlock-freedom* and ***protocol compliance***. The MPST theory guarantees that the code that we generate complies with the inferred protocol (Theorem 5.2), which greatly simplifies the proof of ***extensional*** equivalence (Theorem 5.3), by allowing us to focus on representative traces, instead of all possible interleavings of actions. On the practical side, we perform message reordering optimisation based on the global types [57]. Moreover, an *explicit* representation of the communication protocol is a valuable output for programmers, since it can be used to assess a parallelisation. (Fig. 4).

1.2 Outline and Contributions

§2 defines the Algebraic Functional Language (Alg), a language inspired by the algebra of programming, that we use as a basis for our work; **§3** proposes the Parallel Algebraic Language (PAlg), our front-end language, as an extension of Alg with participant annotations; **§4** introduces a *protocol inference relation* that associates PAlg expressions with MPST protocols, specified as global types. We prove that the inferred protocols are deadlock-free: i.e. every *send* has a matching *receive*. Moreover, we use the global types to justify message reordering optimisations, while preserving communication safety; **§5** develops a translation scheme which *generates* message-passing code from PAlg, that we prove to preserve the extensionality of the input programs; **§6** demonstrates our approach using a number of examples. We will provide as an ***artifact*** our working prototype implementation, and the examples that we used in §6, with instructions on how to replicate our experiments.

2 Algebraic Functional Language

This section describes the Algebraic Functional Language (Alg) and its combinators. In functional programming languages, it is common to provide these combinators as abstractions defined in a base language. For example, one such combinator is the *split* function ($\triangle$), also known as *fanout*, or `(&&&)`, in the *arrow* literature [45] and `Control.Arrow` Haskell package [61]. Programming in terms of these combinators, avoiding explicit mention of variables is known as *point-free* programming. Another approach is to translate code written in a *pointed* style, i.e. with explicit use of variables, to a point-free style [23; 44]. This translation can be fully automated [23; 29]. In our approach, we define common point-free combinators as syntactic constructs of Alg, and require programs to be implemented in this style. Our implementation provides a layer of syntactic sugar for programmers to refer to variables explicitly, as shown in `msort` in §1, but that builds internally a point-free representation.

2.1 Syntax

$$
\begin{aligned}
F_1, F_2 &::= \mathsf{I} \mid \mathsf{K}a \mid F_1 + F_2 \mid F_1 \times F_2 \\
a, b &::= 1 \mid \mathsf{int} \mid \ldots \mid a \to b \mid a + b \mid a \times b \mid F\ a \mid \mu F \\
e_1, e_2 &::= f \mid v \mid \mathsf{const}\ e \mid \mathsf{id} \mid e_1 \circ e_2 \mid \pi_i \mid e_1 \triangle e_2 \mid \iota_i \mid e_1 \triangledown e_2 \\
&\quad \mid F\ e \mid \mathsf{in}_F \mid \mathsf{out}_F \mid \mathsf{rec}_F\ e_1\ e_2
\end{aligned}
$$

In our syntax, $f_1, f_2, \ldots$, capture ***atomic functions***, which are functions of which we only know their types; v_1, v_2 are ***values*** of primitive types (e.g. integer and boolean); $e_1, e_2, \ldots$, represent ***expressions***; $F_1, F_2, \ldots$, are ***functors***; and $a, b, \ldots$, are ***types***. The syntax and semantics are standard [34; 53].

Constant, ***identity functions***, and ***function composition*** are const, id and $\circ$ respectively. ***Products*** are represented using the standard pair notation: if $x : a$ and $y : b$, then $(x, y) : a \times b$. The functions on product types are π_i and $\triangle$, and they represent, respectively, the ***projections***, and the ***split*** operation: $(f \triangle g)(x) = (f\ x, g\ x)$. ***Coproducts*** have two constructors, the ***injections*** ι_i, that build values of type $a + b$. The $\triangledown$ combinator is the ***case*** operation: $(f_1 \triangledown f_2)(\iota_i\ x) = f_i\ x$. Products and coproducts can be generalised to multiple arguments: $a \times b \times c$ is isomorphic to $a \times (b \times c)$, and to $(a \times b) \times c$. We use $\prod_{i \in [1,n]} a_i$ as notation for the product of more than two types; similarly we use $\sum$ for coproducts. The $\prod$ notation binds tighter than any other construct. Whenever $\forall i, j \in I, a_i = a_j = a$, we use the notation $\prod_n a$ as a synonym for $\prod_{i \in [1,n]} a_i$.

Functors are objects that take types into types, and functions to functions, such that identities and compositions are preserved. In this work, we focus on *polynomial functors* [31], which are defined inductively: I is the identity functor, and takes a type a to itself; $\mathsf{K}b$ is the *constant functor*, and takes any type to b; $F_1 \times F_2$ is the *product functor*, and takes a type a to $F_1\ a \times F_2\ a$; $F_1 + F_2$ is the *coproduct functor*, and takes a type to a coproduct type. A term $F\ e$ behaves as *mapping* term e to the I positions in F. For example, if $F = \mathsf{K}a \times \mathsf{I} \times \mathsf{I}$, then applying $F\ e$ to $(x,\ y,\ z)$ yields $(x,\ e\ y,\ e\ z)$.

Recursion is captured by combinators in, out, rec, and type μF. We use standard isorecursive types [31; 47; 53], where μF is *isomorphic* to $F\mu F$, and the isomorphism is given by the combinators in_F (***roll***) and out_F (***unroll***). For any polynomial functor F, μF, and strict functions in_F and out_F are guaranteed to exist. In our implementation, in_F is just a constructor (like inj_i). Recursion is $\mathsf{rec}_F\ e_1\ e_2$, and it is known as a ***hylomorphism*** [53]. A hylomorphism captures a divide-and-conquer algorithm, with a structure described by F, where e_1 is the *conquer* term and e_2 the *divide* term. Using hylomorphisms requires us to work in a semantic interpretation with *algebraic compactness*, i.e. in which carriers of initial F-algebras and terminal F-coalgebras coincide (or are isomorphic). Hylomorphisms and exponentials $\mathsf{ap} : (a \to b) \times a \to b$ allow the definition of a general fixpoint operator [54]. Working with hylomorphisms implies that our input programs may not terminate. We guarantee that, given a *terminating* input program, we will *not* produce a non-terminating parallelisation (Theorem 5.3).

Example 2.1 (MergeSort in Alg)**.** Assume a type Ls of lists of elements of type a. Functor $T = \mathsf{K}\ (\mathsf{Ls}) + \mathsf{I} \times \mathsf{I}$ captures the recursive structure of $\mathbf{ms} : \mathsf{Ls} \to \mathsf{Ls}$. When splitting some $l : \mathsf{Ls}$, we may find one of the two cases described by T: an empty or singleton list, Ls, or a list of size ≥ 2, that can be split in two halves $\mathsf{Ls} \times \mathsf{Ls}$. Assume that a functions $\mathsf{spl} : \mathsf{Ls} \to T\ \mathsf{Ls}$, and a function $\mathsf{mrg} : T\ \mathsf{Ls} \to \mathsf{Ls}$. We define $\mathbf{ms} = \mathsf{rec}_T\ \mathsf{mrg}\ \mathsf{spl}$. By the definition of rec:

$$\begin{aligned}\mathbf{ms} &= \mathsf{rec}_T\ (\mathsf{id} \triangledown \mathsf{mrg})\ \mathsf{spl} = (\mathsf{id} \triangledown \mathsf{mrg}) \circ\ T\ (\mathsf{rec}_T\ \mathsf{mrg}\ \mathsf{spl}) \circ \mathsf{spl}\\ &= (\mathsf{id} \triangledown \mathsf{mrg}) \circ\ (\mathsf{id} + (\mathsf{rec}_T\ \mathsf{mrg}\ \mathsf{spl}) \times (\mathsf{rec}_T\ \mathsf{mrg}\ \mathsf{spl})) \circ \mathsf{spl}\\ &= (\mathsf{id} \triangledown \mathsf{mrg} \circ (\mathbf{ms} \times \mathbf{ms})) \circ \mathsf{spl}\end{aligned}$$

Function **ms** first applies spl. Then, if the list was empty or singleton, it returns the input unmodified. Otherwise, **ms** applies recursively to the first and second halves. Finally, mrg returns a pair of sorted lists.

3 Parallel Algebraic Language

In the previous section we introduced Alg, a point-free functional language. In this section, we extend this language with *participant annotations*. Annotations occur both at the type and expression levels: at the type level, annotations represent *where* the data of the respective type is; at the expression level, it represents *by whom* the computation is performed. This language extension is called PAlg.

The implicit *dataflow* of the Alg (or PAlg) constructs determines which interactions must take place to evaluate an annotated program. To illustrate this, we use the Cooley-Tukey Fast-Fourier Transform algorithm [21]. The Cooley-Tukey algorithm is based on the observation that an FFT of size n, $\mathbf{fft}_n$ can be described as the combination of two FFTs of size $n/2$. We focus its high-level structure:

$$(\mathsf{add}@\mathsf{p}_1 \triangle \mathsf{sub}@\mathsf{p}_2) \circ ((\mathbf{fft}_{n/2}@\mathsf{p}_3 \circ \pi_1) \triangle ((\exp \circ \mathbf{fft}_{n/2})@\mathsf{p}_4 \circ \pi_2))$$

Assume that the input is a pair of vectors that contain the *deinterleaved* input, i.e. elements at even positions on the left, and odd positions on the right. We first compute the **fft** of size $n/2$ to the even and odd elements at p_3 and p_4 respectively. Then, the first half of the output is produced by adding the results pairwise (at p_1), and the second half by subtracting them (at p_2). In order to evaluate this expression, we need to know where is the input data. This is specified by the programmer as an annotated type, which we call ***interface***. Suppose that the interface specifies that the even elements are at p, and the odd elements at p′. The interface that represents this scenario is $(\mathsf{vec} \times \mathsf{vec})@(\mathsf{p} \times \mathsf{p}')$, i.e. an annotated pair of vectors, with the first component at p, and the second component at p′. By keeping track of the locations of the data, we obtain type $(\mathsf{vec} \times \mathsf{vec})@(\mathsf{p}_1 \times \mathsf{p}_2)$, which is the *output* (or *codomain*) interface the PAlg expression. We also refer to the annotations (e.g $\mathsf{p}_1 \times \mathsf{p}_2$) as interfaces, whenever there is no ambiguity. We write $\mathbf{fft}_n : (\mathsf{vec} \times \mathsf{vec})@(\mathsf{p} \times \mathsf{p}') \to (\mathsf{vec} \times \mathsf{vec})@(\mathsf{p}_1 \times \mathsf{p}_2)$ to represent the input and output interfaces of $\mathbf{fft}_n$.

Consider now $e_1@\mathsf{p}_1 \triangledown e_2@\mathsf{p}_2$. The output interface of this expression is either p_1 or p_2, depending on whether the input is the result of applying ι_1 or ι_2. We represent such interfaces using *unions*: $e_1@\mathsf{p}_1 \triangledown e_2@\mathsf{p}_2 : (a + b)@\mathsf{p} \to c@(\mathsf{p}_1 \cup \mathsf{p}_2)$. Since p contains a value of a sum type $a + b$, p is responsible for notifying both p_1 and p_2 which branch needs to be taken in the

control flow. Incorrectly notifying the necessary participants will produce incorrect parallelisations that might deadlock. For example, consider the expression $e_0@\mathsf{p}_0 \circ (e_1@\mathsf{p}_1 \triangledown e_2@\mathsf{p}_2)$. Assuming that the input at p, p needs to notify p_0, otherwise p_0 will be stuck. To avoid such cases, and to compute the interfaces of an expression, we define a type system for PAlg.

3.1 Syntax of PAlg

$$I ::= \mathsf{p} \mid \iota_i\ I \mid I \times I \qquad R ::= I \mid R \cup^{\vec{\mathsf{p}}} R \qquad P ::= R \to R$$
$$\mathsf{e} ::= e@\mathsf{p} \mid [\mathsf{p} \oplus \vec{\mathsf{p}}] \mid \mathsf{id} \mid \mathsf{e} \circ \mathsf{e} \mid \pi_i \mid \mathsf{e} \triangle \mathsf{e} \mid \iota_i \mid \mathsf{e} \triangledown \mathsf{e}$$

The syntax of PAlg is that of Alg, extended with participant annotations (red). Note that certain Alg constructs can only occur under annotations ($e@\mathsf{p}$), e.g: in, out and rec. This implies that recursive functions need to be annotated at a single participant. To parallelise recursive functions, they need first to be rewritten into a suitable form, and then annotate the resulting expression. At the moment, we support automatic recursion unrolling up to a user-specified depth. We provide an overview of the main syntactic constructs of PAlg: ***annotations***, ***interfaces***, and ***annotated functions***.

Annotations are ranged over by R, R',... We define them in two layers, I, or simple annotations that cannot contain choices ($\cup$), and R. This way, we ensure that choices only occur at the topmost level. Simple annotations are: participant ids p, that identify processes; products of interfaces $I_1 \times I_2$; and tagged interfaces $\iota_i\ I$, that keep track of the branch of the choice that led to I. A choice $R_1 \cup^{\vec{\mathsf{p}}} R_2$ describes an scenario that is the result of a branch in the control flow, where a value can be found at either R_1 or R_2. Here, $\vec{\mathsf{p}} = \mathsf{p}_1 \cdots \mathsf{p}_n$ are the participants whose behaviour depends on the path in the control flow. Finally, arrows P of the form $R_1 \to R_2$ represent the input/output annotations of a parallel program.

Interfaces are annotated types. They range over A, B, ..., and are of the form $a@R$, which means that values of type a are distributed across R. We require annotated types to be *well-formed*, $\mathsf{WF}(a@R)$, which implies that the structure of a matches that of R. We write $\mathcal{I}$ to represent *one-hole contexts* for interfaces, with $\mathcal{I}[\mathsf{p}]$ representing the interface that results of placing p at the hole in $\mathcal{I}$.

Annotated functions are ranged over by e, e′. The annotations are introduced using $e@\mathsf{p}$, where e is an unannotated Alg expression, and p is a single participant identifier. These annotations need to be set by the programmer, but their introduction can be also automated. Additionally, we introduce the choice point annotations: $[\mathsf{p} \oplus \vec{\mathsf{p}}]$. This annotation specifies that p performs a choice, and notifies $\vec{\mathsf{p}}$. Choice points can be introduced fully automatically by collecting all participants whose behaviour depends on the value of a sum type.

3.2 Interfaces

An interface represents a *state* in a concurrent system: the set of participants, and the types of the values that they contain. We use mappings from participants to values to represent such states: $V := [\mathsf{p} \mapsto v]_{\mathsf{p} \in \mathcal{P}}$. The programmer, additionally to writing an Alg (PAlg) expression, will need to provide an *input interface*, i.e. *where* is the input to the parallel program. Consider, for example, the interface $\mathsf{int}@\mathsf{p}_i$. Given a concurrent system with participants $\mathsf{p}_o \cdots \mathsf{p}_n$, we know that p_i contains a value of type int: $[\cdots\ \mathsf{p}_i \mapsto 42\ \cdots]$. An interface with a product of participants $(a \times b)@(\mathsf{p}_1 \times \mathsf{p}_2)$ represents a state in which p_1 contains an element of type a, and p_2 an element of type b, e.g a possible state represented by $(\mathsf{int} \times \mathsf{vec})@(\mathsf{p}_1 \times \mathsf{p}_2)$ is: $[\cdots \mathsf{p}_1 \mapsto 42 \cdots \mathsf{p}_2 \mapsto [1, 1, 2, \ldots] \cdots]$. An interface $\iota_i\ I$ represents the same state as interface I, but we statically know that this state was reached after an i-th injection. Then, if a participant requires the value at I, this participant will apply the necessary injections to the received values. Finally, an interface $a@(R_1 \cup^{\vec{\mathsf{p}}} R_2)$ means that the state might be either R_1 or R_2, and that all participants $\vec{\mathsf{p}}$ should be notified of the state.

Well-formedness The above examples are of well-formed interfaces: $\mathsf{int}@\mathsf{p}_i$, $(\mathsf{int} \times \mathsf{vec})@(\mathsf{p}_1 \times \mathsf{p}_2)$. Well-formedness ensures that interfaces represent valid states. Generally, $a@R$ is well-formed if a matches the structure of R. For example, $\mathsf{int}@(\mathsf{p}_1 \times \mathsf{p}_2)$ is *ill-formed*, since a *single* integer cannot be at *two* different participants. An interface $a@(R_1 \cup R_2)$ requires that both $a@R_1$ and $a@R_2$ are well-formed. So, $(\mathsf{vec} \times \mathsf{vec})@((\mathsf{p}_1 \times \mathsf{p}_2) \cup \mathsf{p}_3)$ is well-formed because we can have $\mathsf{vec}@\mathsf{p}_1$ and $\mathsf{vec}@\mathsf{p}_2$, or $(\mathsf{vec} \times \mathsf{vec})@\mathsf{p}_3$. However, $\mathsf{int}@((\mathsf{p}_1 \times \mathsf{p}_2) \cup \mathsf{p}_3)$ is ill-formed, because $\mathsf{int}@(\mathsf{p}_1 \times \mathsf{p}_2)$ is ill-formed.

3.3 Typing of Parallel Algebraic Language

We introduce a relation that associates Alg expressions with potential parallelisations PAlg, and their interfaces. This relation can be seen as a type system for both Alg and PAlg. As a type system for PAlg, this relation provides a way to check or infer the output interface of some e. By using this relation as a type system for Alg, we can explore potential parallelisations of some input expression e. Additionally, the type system ensures that all *choice point* annotations contain every participant that depends on each particular choice.

Typing Rules A judgement of the form $\vdash e \Rightarrow \mathsf{e} : A \to B$ means that the PAlg expression e is one potential parallelisation of the Alg expression e, with domain interface A and codomain interface B. The intuition of a judgement $\vdash e \Rightarrow \mathsf{e} : a@R_1 \to b@R_2$ is that the participants in e collectively apply computation e to the value of type a distributed across R_1, and produce a value of type b distributed across R_2. We sometimes omit e and write $\vdash \mathsf{e} : A \to B$. We ensure that given any e and e such that they are typeable against interfaces $a@R_a \to b@R_b$, then e must have type $a \to b$.

Lemma 3.1. *If* $e \Rightarrow \mathsf{e} : a@R_a \to b@R_b$, *then* $e : a \to b$.

The typing rules (Fig. 2) must ensure that the participants involved in a choice are notified, and that Alg expressions

are correctly expanded. Rule Choice specifies that a choice point may be introduced at any point when a participant contains a value of a sum-type. In such cases p sends the tag of the sum-type value to any other participant whose behaviour depends on it. After the choice point, the interface is $I[\iota_1\ \mathsf{p}] \cup^{\vec{\mathsf{p}}} I[\iota_2\ \mathsf{p}]$, with the constraint that the participants in $\mathcal{I}[\mathsf{p}]$ must be in $\vec{\mathsf{p}}$. Rule Alt specifies that e must be the parallelisation of e, considering both A_1 and A_2 as input interfaces. The output interface is the union of B_1 and B_2. Any participant in e must be notified of the choice $\mathsf{pids}(\mathsf{e}) \subseteq \vec{\mathsf{p}}$, to make sure that they perform the interactions that correspond to the correct A_i. Rule Alg specifies that given any e and participant p, $e@\mathsf{p}$ is a valid parallelisation, with output interface $b@\mathsf{p}$. Finally, rule Ext is crucial for exploring potential parallelisations. It states that if e is the parallelisation of e_2, and e_2 is extensionally equal to e_1, then e is also a parallelisation of e_1. The undecidability of this rule requires that the programmer specifies *rewriting strategies* both for checking and inference.

Rewriting and Annotation Strategies We use rewriting strategies when exploring potential parallelisations of functions. This is inference problem (2) below. Let **?i** be metavariables. The two inference problems that we are interested in are: 1. Solving $\vdash e \Rightarrow \mathsf{e} : A \to$ **?0** obtains the output interface for e, with input interface A. 2. Solutions of $\vdash e \Rightarrow$ **?0** $: A \to$ **?1** are potential parallelisations of e, and their output interface. Solving (1) is straightforward. Problem (2) requires to decide how to introduce role annotations (rule Alg), how to perform rewritings (rule Ext), and where to introduce choice points (rule Choice). Introducing choice points is straightforward: we introduce them as early as possible, as soon as an input interface contains a sum-type at a participant. For introducing annotations and doing Alg rewritings, the programmer has to specify *annotation* and *rewriting* strategies. At the moment, our tool allows the developer to introduce annotations explicitly, or to select sub-expressions that will be annotated with fresh new participants. The rewriting strategies that our current implementation supports are unrollings of recursive definitions. However, our tool is extensible: the equivalences used in the rewritings are a parameter.

Example 3.2 (Mergesort)**.** Consider the mergesort definition **ms** $= \mathsf{rec}_T$ mrg spl. Solutions to the inference problem $\vdash$ **ms** $\Rightarrow$ **?0** $: \mathsf{Ls}@\mathsf{p}_0 \to$ **?1** provide the alternative parallelisations of **ms**. By choosing a rewriting strategy that unrolls **ms** once, and annotates any remaining instances of **ms** at fresh new participants, we produce the following PAlg expression:

$$\begin{aligned}&\vdash (\mathsf{id} \triangledown (\mathsf{mrg} \circ (\mathbf{ms} \times \mathbf{ms}))) \circ \mathsf{spl}\\ &\quad\Rightarrow (\mathsf{id} \triangledown (\mathsf{mrg}@\mathsf{p}_1 \circ (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1)))\\ &\qquad\circ[\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \circ \mathsf{spl}@\mathsf{p}_1\\ &\quad: \mathsf{Ls}@\mathsf{p}_0 \to \mathsf{Ls}@\mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \mathsf{Ls}@\mathsf{p}_1\end{aligned}$$

$$\textsc{Alg}\ \frac{\vdash e : a \to b}{\vdash e \Rightarrow e@\mathsf{p} : a@I \to b@\mathsf{p}} \qquad \textsc{Ext}\ \frac{\vdash e_2 \Rightarrow \mathsf{e} : a@I \to B \qquad e_1 =_{\mathsf{ext}} e_2}{\vdash e_1 \Rightarrow \mathsf{e} : a@I \to B}$$

$$\textsc{Alt}\ \frac{\vdash e \Rightarrow \mathsf{e} : A_1 \to B_1 \quad \vdash e \Rightarrow \mathsf{e} : A_2 \to B_2 \quad A_1 \neq A_2 \quad \mathsf{pids}(\mathsf{e}) \subseteq \vec{r}}{\vdash e \Rightarrow \mathsf{e} : A_1 \cup^{\vec{\mathsf{p}}} A_2 \to B_1 \cup^{\vec{\mathsf{p}}} B_2}$$

$$\textsc{Choice}\ \frac{\mathsf{pids}(\mathcal{I}[\mathsf{p}]) \subseteq \vec{\mathsf{p}} \qquad \mathsf{WF}(a@\mathcal{I}[\iota_i\ \mathsf{p}]), i \in [1,2] \quad \vdash e \Rightarrow \mathsf{e} : a@(\mathcal{I}[\iota_1\ \mathsf{p}] \cup^{\vec{\mathsf{p}}} \mathcal{I}[\iota_2\ \mathsf{p}]) \to B}{\vdash e \Rightarrow \mathsf{e} \circ [\mathsf{p} \oplus \vec{\mathsf{p}}] : a@\mathcal{I}[\mathsf{p}] \to B}$$

Figure 2. Typing rules of PAlg (selected)

4 Multiparty Session Types for PAlg

The dataflow of the PAlg constructs determine the communication protocol of the annotated expression. However, it is hard to manually check what this communication structure is. Recall the mergesort PAlg expression of §3, **ms**, and suppose that we want to produce a parallelisation for a 32-core machine. Then, we might be interested in using a 5-unfolding of **ms**, so that we have **ms** executing concurrently on all of the cores. How do we know, for such cases, that we produced a *sensible* parallelisation? As an example, suppose we use an annotation strategy that produces the following code:

$$\begin{aligned}&(\mathsf{id} \triangledown (\mathsf{mrg}@\mathsf{p}_1 \circ (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_2 \circ \pi_2@\mathsf{p}_1)))\\ &\quad\circ[\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2] \circ \mathsf{spl}@\mathsf{p}_1 : \mathsf{Ls}@\mathsf{p}_0 \to \mathsf{Ls}@\mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2} \mathsf{Ls}@\mathsf{p}_1\end{aligned}$$

Notice that this example will run correctly, and produce the expected result. However, the achieved PAlg expression is *not* parallel! If we represent the ***implicit dataflow*** of this expression as ***explicit communication***, the reason becomes apparent. We use *global types* from *multiparty session types* to provide an explicit representation of the communication structure of the program:

$$\begin{aligned}&\mathsf{p}_0 \to \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{p}_1 \to \mathsf{p}_2\{\iota_1.\ \mathsf{end};\\ &\quad\iota_2.\ \mathsf{p}_1 \to \mathsf{p}_2 : \mathsf{Ls}.\ \mathsf{p}_1 \to \mathsf{p}_2 : \mathsf{Ls}.\mathsf{p}_2 \to \mathsf{p}_1 : \mathsf{Ls} \times \mathsf{Ls}.\ \mathsf{end}\}\end{aligned}$$

This global type represents the following protocol: 1. participant p_0 sends a list to p_1; 2. p_1 sends to p_2 either ι_1 or ι_2, and if the label is ι_1, the protocol ends; 3. if p_1 sent ι_2, then p_1 sends to p_2 *two* lists, in two different interactions; and 4. p_2 replies with a message to p_1 with a pair of lists. It is clear from this protocol that p_1 and p_2 are dependent on each others' messages, and that p_2 cannot perform any computation in parallel. The larger the expression is, the harder avoiding these wrong annotations will become. By changing the annotation strategy, we produce the following parallel structure, where p_2 and p_3 can operate in parallel:

$$\begin{aligned}&\mathsf{p}_0 \to \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{p}_1 \to \{\mathsf{p}_2\mathsf{p}_3\}\ \{\iota_1.\ \mathsf{end};\\ &\quad\iota_2.\ \mathsf{p}_1 \to \mathsf{p}_2 : \mathsf{Ls}.\ \mathsf{p}_1 \to \mathsf{p}_3 : \mathsf{Ls}.\mathsf{p}_2 \to \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{p}_3 \to \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{end}\}\end{aligned}$$

This abstraction of the communication protocol of an achieved parallelisation is therefore useful as an output for the programmer. Additionally, these global types are a *contract* that

can be enforced on the generated code. We use this for proving that our back-end is correct, but also for applying low-level code optimisations (e.g. message reordering) guided by this global type, ensuring that they do not introduce any run-time error. For example, when we find in a global type $\mathsf{p}_1 \to \mathsf{p}_2.\ \mathsf{p}_2 \to \mathsf{p}_3$, we mark the send/receive actions for p_2 as point of potential optimisation. If the messages exchanged do not depend on each other, we permute them, performing first the send action, so that p_2 is not blocked by a receive action. This is known as *asynchronous optimisation* [57].

4.1 Multiparty Session Types

Our global types are based on the most commonly used in the literature [22]. We start with a set of *participant identifiers*, $\mathsf{p}_1, \mathsf{p}_2, \ldots$, and a set of *labels*, $\iota_1, \iota_2, \ldots$. These are considered as natural numbers: participant identifiers uniquely identify an independent unit of computation, e.g. thread or process ids; and labels are tags that differentiate branches in the data/control flow. The syntax of global (G) and local (L) types in MPST is given as:

$$G ::= \mathsf{p}_1 \to \mathsf{p}_2 : a.G \mid \mathsf{p}_1 \to \{\mathsf{p}_j\}_{j\in[2,n]} : \{\iota_i.G_i\}_{i\in I} \mid \mu X.G \mid X \mid \mathsf{end}$$

$$L ::= \mathsf{p}!\langle a\rangle.L \mid \mathsf{p}?(a).L \mid \mathsf{p}\ \&\ \{\iota_i.L_i\}_{i\in I} \mid \{\mathsf{p}_j\}_{j\in[2,n]} \oplus \{\iota_i.L_i\}_{i\in I} \mid \mu X.L \mid X \mid \mathsf{end}$$

Global type $\mathsf{p}_1 \to \mathsf{p}_2 : a.G$ denotes *data* interactions from p_1 to p_2 with value of type a; *Branching* is represented by $\mathsf{p}_1 \to \{\mathsf{p}_j\}_{j\in[2,n]} : \{\iota_i.G_i\}_{i\in I}$ with actions ι_i from p_1 to all $\mathsf{p}_j, j \in [2, n]$. end represents a *termination* of the protocol. $\mu X.G$ represents a *recursive* protocol, which is *equivalent* to $[\mu X.\ G/X]G$. We assume recursive types are guarded.

Each participant in G represents a different participant in a parallel process. *Local session types* represent the communication actions performed by each participant, i.e. the *role* of the participant. Since each participant has a unique role, we sometimes refer to them interchangeably. The *send* type $\mathsf{p}!\langle a\rangle.L$ expresses the action of sending of a value of type a to p followed by interactions specified by L. The *receive* type $\mathsf{p}?(a).L$ is the dual, where a value with type a is received from p. The *selection* type represents the transmission to all p_j of label ι_i chosen in the set of labels ($i \in I$) followed by L_i. The *branching* type is its dual. $\mathsf{pids}(G)/\mathsf{pids}(L)$ denote the set of participants that occur in G/L.

Projection We use a standard definition of *projection* that uses the *full merging* operator [24; 27], which allows more well-formed global types than the original projection rules [42]. We write $G \upharpoonright \mathsf{p}$ for the projection of G onto the role of p. We illustrate the projection with an interaction $\mathsf{p}_0 \to \mathsf{p}_1 : a.G$. The projection onto p_0 is $\mathsf{p}_1!\langle a\rangle.(G \upharpoonright \mathsf{p}_0)$, the projection onto p_1 is $\mathsf{p}_0?(a).(G \upharpoonright \mathsf{p}_1)$, and the projection onto any other role p is $G \upharpoonright \mathsf{p}$. Projection on choices is similar, with the difference that whenever the role is not at the receiving or sending ends of the choice, the different branches must be *merged*. Two local types can be merged when they are the same, or they branch on the same role, and their continuations can be merged.

We use a standard definition of *well-formedness* that states that a global type is well formed if ts projection on all its roles is defined. We denote: $\mathsf{WF}(G) = \forall \mathsf{p} \in \mathsf{pids}(G), \exists L, G \upharpoonright \mathsf{p} = L$.

$$\textsc{Choice}\ \frac{}{\vDash [\mathsf{p} \oplus \vec{\mathsf{p}}] \Leftarrow a[b+c]@I[\mathsf{p}] \sim \mathsf{p} \to \{\vec{\mathsf{p}} \setminus \mathsf{p}\}\{\iota_1.\ \mathsf{end};\ \iota_2.\ \mathsf{end}\}}$$

$$\textsc{Alt}\ \frac{\vDash \mathsf{e} \Leftarrow A_1 \sim G_1 \quad \vDash \mathsf{e} \Leftarrow A_2 \sim G_2}{\vDash \mathsf{e} \Leftarrow A_1 \cup^{\vec{\mathsf{p}}} A_2 \sim G_1 \cup G_2} \qquad \textsc{Alg}\ \frac{\vdash e : a \to b}{\vDash e@\mathsf{p} \Leftarrow a@I \sim [a@I \rightsquigarrow \mathsf{p}]}$$

$$[a@\mathsf{p}_1 \rightsquigarrow \mathsf{p}_2] = \mathsf{p}_1 \to \mathsf{p}_2 : a.\ \mathsf{end}, \text{ if } \mathsf{p}_1 \neq \mathsf{p}_2;\ [a@\mathsf{p} \rightsquigarrow \mathsf{p}] = \mathsf{end};$$
$$[(a \times b)@(I_a \times I_b) \rightsquigarrow \mathsf{p}] = [a@I_a \rightsquigarrow \mathsf{p}] \fatsemi [b@I_b \rightsquigarrow \mathsf{p}];\ \text{and}$$
$$[(a_1 + a_2)@(\iota_i\ I) \rightsquigarrow \mathsf{p}] = [a_i@I \rightsquigarrow \mathsf{p}]$$

Figure 3. Protocol Relation (selected)

4.2 Protocol Relation

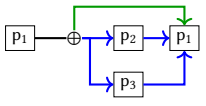
We introduce now the set of rules that associate a PAlg expression and domain interface with their global type (Fig. 3). We extend the syntax of global types with $G_1 \cup^{\vec{\mathsf{p}}} G_2$ to represent the *external choices*, i.e. G_i are the continuations for both branches of a previous choice that affects $\vec{\mathsf{p}}$. We also extend the local types, and projection rules $(G_1 \cup^{\vec{\mathsf{p}}} G_2) = G_1 \upharpoonright \mathsf{p} \cup^{\vec{\mathsf{p}}} G_2 \upharpoonright \mathsf{p}$, and the notion of well-formedness. We say that an external choice is well-formed, $\mathsf{WF}(G_1 \cup^{\vec{\mathsf{p}}} G_2)$, if $\mathsf{WF}(G_1)$, $\mathsf{WF}(G_2)$, and for all $\mathsf{p} \notin \vec{\mathsf{p}}$, $G_1 \upharpoonright \mathsf{p} = G_2 \upharpoonright \mathsf{p}$. We omit the annotation of the participants involved in the choice whenever it is not needed. The relation $\vDash p \Leftarrow A \sim (G, B)$ specifies that the parallel code for p and input interface A will behave as global type G, and output interface B (Fig. 3). The rules are similar to the typing rules of PAlg.

Example 4.1 (Mergesort Protocol)**.** The protocol for Example 3.2 is obtained by solving:

$\vDash (\mathsf{id} \triangledown (\mathsf{mrg}@\mathsf{p}_1 \circ (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \triangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1))) \circ [\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \circ \mathsf{spl}@\mathsf{p}_1 \Leftarrow \mathsf{Ls}@\mathsf{p}_1 \sim \mathbf{?0}.$

$$\mathsf{p}_1 \to \{\mathsf{p}_2\mathsf{p}_3\} \left\{ \begin{array}{l} \iota_1.\mathsf{end}; \\ \iota_2.\mathsf{p}_1 \to \mathsf{p}_2 : \mathsf{Ls}.\mathsf{p}_1 \to \mathsf{p}_3 : \mathsf{Ls}.\mathsf{end} \end{array} \right\} \fatsemi (\mathsf{end} \cup (\mathsf{p}_2 \to \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{p}_3 \to \mathsf{p}_1 : \mathsf{Ls}.\mathsf{end}))$$

$$= \mathsf{p}_1 \to \{\mathsf{p}_2\mathsf{p}_3\} \left\{ \begin{array}{l} \iota_1.\mathsf{end}; \\ \iota_2.\mathsf{p}_1 \to \mathsf{p}_2 : \mathsf{Ls}. \\ \quad \mathsf{p}_1 \to \mathsf{p}_3 : \mathsf{Ls}. \\ \quad \mathsf{p}_2 \to \mathsf{p}_1 : \mathsf{Ls}. \\ \quad \mathsf{p}_3 \to \mathsf{p}_1 : \mathsf{Ls}.\mathsf{end} \end{array} \right\}$$



4.3 Correctness

We guarantee that for e s.t. $\vdash e \Rightarrow \mathsf{e} : A \to B$, with A and B well-formed, there exists a protocol G and that it is well-formed and deadlock-free.

Lemma 4.2. *[Existence of Associated Global Type] For all* $\mathsf{WF}(A)$*, if* $\vdash \mathsf{e} : A \to B$*, then there exists* G *s.t.* $\vDash \mathsf{e} \Leftarrow A \sim G$.

Lemma 4.3. *[Protocol Deadlock-Freedom] For all* $\mathsf{WF}(A)$*, if* $\vdash \mathsf{e} : A \to B$ *and* $\vDash \mathsf{e} \Leftarrow A \sim G$*, then* $\mathsf{WF}(G)$.

Remark. *Since the local type abstracts the behaviour of multiparty typed processes, a well-formed global type ensures the end-point processes (programs) typed by that global type are guaranteed to satisfy the properties (such as safety and deadlock-freedom) of local types [27; 43].*

5 Code Generation

This section addresses the problem of generating low-level parallel code from PAlg expressions. We prove that *the generated code complies with its inferred protocol*, which has several implications: (1) code generation does not introduce any concurrency errors, and the parallel code is therefore deadlock-free; and (2) we can prove that *the generated code is extensionally equal to the input expression* by considering only a representative trace, since any valid interleaving of actions must respect this protocol. The target language of our tool is an indexed monad, the *Message Passing Monad* (Mp). From Mp, we implement our low-level C backend. We implement an untyped version of Mp as a deep embedding in Haskell, and session typing on top of it. This is suitable for code generation: we only generate parallel code if the monadic actions are typeable against the respective local types. Our definition of Mp has significant differences to other embeddings of session types in Haskell, such as the Session monad by Neubauer and Thiemann [58]. First, our Mp monad is deeply embedded in Haskell, and secondly, we use type indices instead of an encoding of session types in terms of type classes. Our approach is better suited for compilation since we manipulate session types, and postpone session typing until code generation.

5.1 Message Passing Monad

Mp comprises four basic operations: send, receive, choice and branching, with a standard (asynchronous) semantics. Additionally, for composing actions that depend on the same choice, we introduce case expressions. Our definition of Mp is based on the *free monad* construction:

$$
\begin{aligned}
v &::= x \mid (v, v) \mid \iota_i\ v \mid \cdots \mid e\ v \\
m_i &::= \mathbf{ret}\ v \mid \mathbf{send}\ \mathsf{p}\ v\ m \mid \mathbf{recv}\ \mathsf{p}\ a\ f \mid \mathbf{sel}\ \vec{\mathsf{p}}\ v\ f_1\ f_2 \\
&\quad \mid \mathbf{brn}\ \mathsf{p}\ m_1\ m_2 \mid \mathbf{case}\ f_1\ f_2 \quad f ::= \lambda x.m
\end{aligned}
$$

Values v are either primitive values, tagged values $\iota_i\ v$, pairs of values, or the result of applying an Alg expression e to a value. We use standard notation for the monadic unit (**ret**), bind ($\gg=$) and Kleisli composition: $f_1 >=> f_2 = \lambda x.\ f_1\ x \gg= f_2$. The message-passing constructs are standard, except **sel**, **brn** and **case**, which are used for performing choices, and composing actions that depend on the same choice.

Each monadic computation f or m has a type $m : \mathsf{Mp}\ L\ a$, where a is the return type of m, and L is the type index of Mp, and it represents the local type that corresponds to the behaviour of the term m. There is almost a one to one correspondence between the terms L and the monadic actions m, so we omit the full definition. The types of the constructs that deal with choices use a new type, $\uplus$, that is isomorphic to sum types, but that can only be constructed and eliminated by using the corresponding monadic constructs:

$$
\begin{aligned}
&\mathbf{sel}\ \vec{\mathsf{p}} : a + b \to (a \to \mathsf{Mp}\ L_1\ c_1) \to (b \to \mathsf{Mp}\ L_2\ c_2) \\
&\qquad \to \mathsf{Mp}\ (\vec{\mathsf{p}} \oplus \{\iota_1.L_1; \iota_2.L_2\})\ (c_1 \uplus c_2) \\
&\mathbf{brn}\ \mathsf{p} : \mathsf{Mp}\ L_1\ a_1 \to \mathsf{Mp}\ L_2\ a_2 \\
&\qquad \to \mathsf{Mp}\ (\mathsf{p}\ \&\ \{\iota_1.L_1; \iota_2.L_2\})\ (a_1 \uplus a_2) \\
&\mathbf{case} : (a \to \mathsf{Mp}\ L_1\ c) \to (b \to \mathsf{Mp}\ L_2\ d) \to a \uplus b \\
&\qquad \to \mathsf{Mp}\ (L_1 \cup L_2)\ (c \uplus d)
\end{aligned}
$$

These constructs ensure that the tag used to build $a \uplus b$ indeed corresponds to the correct branch of the right choice. We use **case** to compose actions that depend on a previous choice. While this treatment of $\uplus$ leads to unnecessary code duplication, our back-end easily optimises cases where we have **case** $f\ f$ to avoid code duplication.

Parallel programs We define the basic constructs of PAlg in a bottom-up way by manipulating *parallel programs*. Parallel programs are mappings from participants to their monadic action: $\mathsf{E} ::= [\mathsf{p}_i \mapsto m_i]_{i \in I}$. If $m_i : \mathsf{Mp}\ L_i\ a_i$ for all $i \in I$, then we write $[\mathsf{p}_i \mapsto m_i]_{i \in I} : \mathsf{Mp}\ [\mathsf{p}_i \mapsto L_i]_{i \in I}\ [\mathsf{p}_i \mapsto a_i]_{i \in I}$. The semantics of both local types and monadic actions is defined in terms of such collections of actions or local types, and shared queues of values W, or queues of types Q, e.g. $\langle \mathsf{E}, W \rangle \rightsquigarrow^{\ell} \langle \mathsf{E}', W' \rangle$ is a transition from E to E', and shared queues W to W' with *observable action* ℓ. We prove a standard safety theorem (Theorem 5.1 below) that guarantees that if a participant does a transition with some observable action, then so does the type index.

Theorem 5.1. *[Soundness] Assume* $\mathsf{E} : \mathsf{Mp}\ C\ A$, $m : \mathsf{Mp}\ L\ a$ *and* $W : Q$*. Suppose* $\langle \mathsf{E}[r \mapsto m], W \rangle \rightsquigarrow^{\ell} \langle \mathsf{E}[r \mapsto m'], W' \rangle$*. Then there exists* $\langle C[r \mapsto L], Q \rangle \to^{\ell} \langle C[r \mapsto L'], Q' \rangle$ *such that* $W' : Q'$ *and* $m' : \mathsf{Mp}\ L'\ a$.

Mp code generation The translation scheme for Mp code generation is done recursively on the structure of PAlg expressions. It takes a PAlg expression e, an interface A, and produces a mapping from all participants in e and A to their respective monadic continuations. We write $[\![\mathsf{e}]\!]\,(A)$, and guarantee that $[\![\mathsf{e}]\!]\,(A) : A \to \mathsf{Mp}\ G\ B$, if $\vDash \mathsf{e} \Leftarrow A \sim (G, B)$. This means that if e induces protocol G with interfaces $A \to B$, then the generated code behaves as G, with interfaces A and B. Code generation follows a similar structure to global type inference, and is defined by building PAlg constructs as Mp parallel programs. For example, the translation of $\mathsf{e@p} : a@I \to B$ requires to define the interactions from an interface I that gathers a type a at p: $(\!|a@I \rightsquigarrow \mathsf{p}|\!) : a@I \to \mathsf{Mp}\ [a@I \rightsquigarrow \mathsf{p}]\ (a@\mathsf{p})$. The definition

is analogous to that of $[a@I \rightsquigarrow \mathsf{p}]$. The remaining of the translation is straightforward, so we skip the details.

We prove two main correctness results. We guarantee that the generated code behaves as its inferred protocol (Theorem 5.2). We also guarantee that regardless of the annotations and interfaces chosen for e, *the parallel code always produces the same result as the sequential implementation* (Theorem 5.3).

Theorem 5.2. *[Protocol Conformance of the Generated Code] If* $\vDash \mathsf{e} \Leftarrow A \sim G$, *then* $[\![\mathsf{e}]\!]\,(A)$ *complies with protocol* G.

Theorem 5.3. *[Extensionality] Assume* $e \Rightarrow \mathsf{e} : a@\mathsf{p} \to b@R$ *and* $x : a$ *initially at* p. *If* $e\ x = y$, *then the execution of* $[\![\mathsf{e}]\!]\,(\mathsf{p})$ *also produces* y, *distributed across* R.

Example 5.4 (MergeSort Code Generation)**.** We show below the code generation for **ms** (Example 3.2), with p_1 as domain interface:

$$\mathsf{p}_1 \mapsto \lambda x.\ \mathbf{sel}\ \{\mathsf{p}_2, \mathsf{p}_3\}\ (\mathrm{spl}\ x)\ (\lambda x.\ \mathbf{ret}\ x)$$
$$(\lambda x.\ \mathbf{send}\ \mathsf{p}_2\ (\pi_1\ x) \gg\!= \lambda y.\ \mathbf{send}\ \mathsf{p}_3\ (\pi_2\ x) \gg\!= \lambda\_.$$
$$\mathbf{recv}\ \mathsf{p}_2\ \mathsf{Ls} \gg\!= \lambda x.\ \mathbf{recv}\ \mathsf{p}_3\ \mathsf{Ls} \gg\!= \lambda y.\ \mathbf{ret}\ (\mathrm{mrg}\ (x, y)))$$
$$\mathsf{p}_{2,3} \mapsto \lambda x.\ \mathbf{brn}\ \mathsf{p}_1\ (\mathbf{ret}\ x)\ (\mathbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\mathbf{send}\ \mathsf{p}_1\ (\mathbf{ms}\ x))$$

6 Parallel Algorithms and Evaluation

We evaluate our approach using a number of parallel algorithms derived from Alg expressions, and the speedups achieved. The purpose of this is twofold: (i) showing that our approach achieves speedups for an input sequential algorithm, with naïve annotation strategies, and limited optimisations (Fig. 5), and (ii) illustrating the practical value of providing a global type that describes the parallel strategy achieved by a particular annotation strategy (Fig. 4). We run all our experiments on 2 NUMA nodes, 12 cores per node and 62GB of memory, using Intel Xeon CPU E5-2650 v4 @ 2.20GHz chips. We run our experiments first restricting the execution to a single node to avoid NUMA effects, and then on the 2 NUMA nodes.

6.1 Benchmarks

Mergesort Mergesort is the usual divide-and-conquer algorithm, using a tree-like parallel reduce.

Cooley-Tukey FFT We use a recursive Cooley-Tukey algorithm. The algorithm starts by splitting the elements of the list into those that are at even and odd positions. Then, it recursively computes the FFT of them, and finally combines the results. To generate a butterfly pattern, we use: products of size n, to store the results of the subsequent interleavings; product associativity to produce a perfect tree; and *asynchronous optimisations*.

Dot Product The dot product algorithm zips the inputs, multiplies them pairwise, and then adds them by folding the result. We use products of size n to derive a scatter-gather.

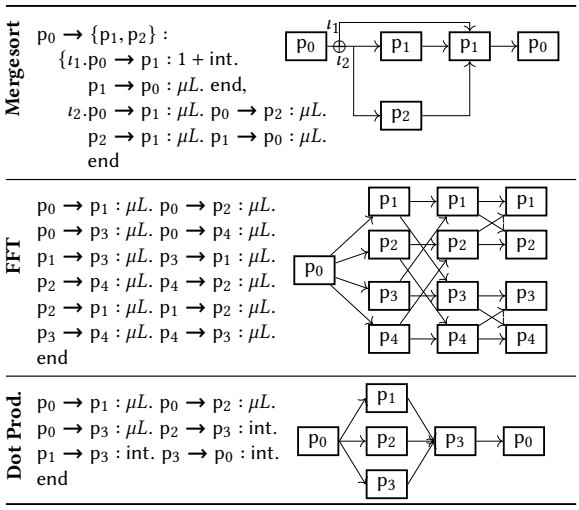


Figure 4. Benchmarks: potential parallelisations.

Additional Algorithms We implemented *scalar prod*, that recursively splits a matrix into sub-matrices, distributes them to different workers, and then multiplies their elements by a scalar, and *quicksort*, with a divide-and-conquer structure.

6.2 Evaluation

We translate Mp monadic actions to C using `pthreads` and shared buffers for communication, and we have a preliminary compilation of the first-order sequential terms to C. We compile the generated C code using gcc version 4.8.5. We take the average of 50 repetitions for each benchmark. Our benchmarks achieve reasonable speedups against the sequential C implementations. Fig. 5 presents the speedups against the number of participants for different input sizes, and Fig. 6 present a summary of our speedups for large inputs of size $> 10^9$. We show below an analysis of these results, by plotting the speedups against two factors: 1. the number of participants (threads) produced by a particular annotation and recursion unrolling, named K; and 2. the input size, e.g. number of elements in the input list.

Increasing the number of threads (parameter K), increases the speedups obtained, up to a certain value that depends on the amount of available cores and the input size. For benchmarks that work better with dynamic task creation, our tool does not currently achieve good performance (e.g. quicksort). For FFT, our tool produces the usual butterfly pattern from a straightforward recursive definition, that we can achieve a speedup of 12 when running on a single shared-memory node. The rest of the examples are limited either by *Amdahl's law* (justified by their global types in Fig. 4), or by the overhead of the communication and pthread creation with respect to the cost of the computations, but still achieve speedups of up to 7 and 8 on 12 cores. We can observe that there is a slow down after creating a much larger number of participants than the ones required. This usually depends on how evenly we can distribute the data amongst workers, and whether

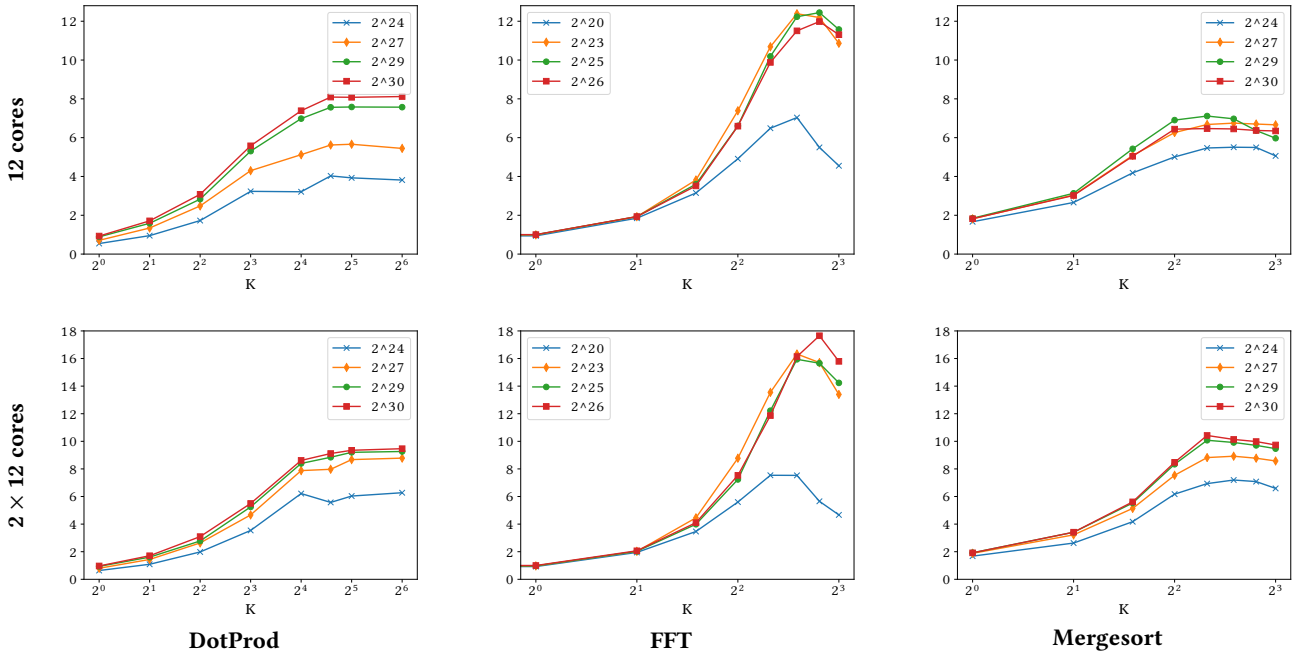


Figure 5. Benchmark speedups, run in 2 NUMA nodes with 12 cores each. The X-axis is the number of workers of the parallel program generated from a set of annotations and recursion unrolling. We show the results for 4 different input sizes.

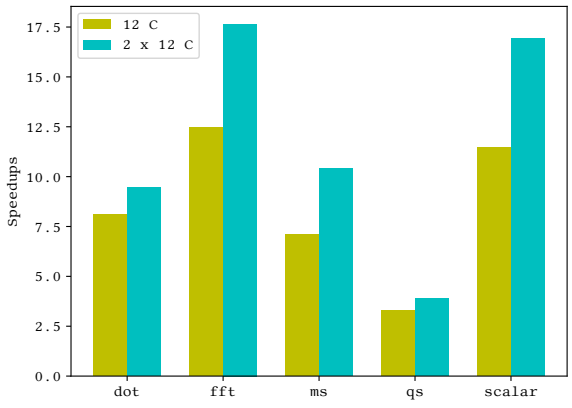


Figure 6. Achieved speedups

the amount of workers can be evenly scheduled to different cores. We observe that we can achieve further speedups when running our benchmarks in the 2 NUMA nodes. Overall, we observe that our annotation strategies enable good speedups over the sequential implementation, with relatively little effort. Global types can be used to detect optimisation opportunities that yield efficient parallelisations, such as the Butterfly topology in Fig. 4. Without message-reordering based on the session types, FFT participant p_3 would need to wait for p_1's message before sending its part to p_1, i.e. p_3's local type would be $p_1?(\mu L).\ p_1!\langle \mu L\rangle \ldots$. This means that p_3's local computation would only become available to p_1 after it p_1 finishes its own local computation, thus sequentialising the code. Asynchronous permutations [16; 57] allow us to *permute* such actions, and still have communication safety, i.e. $p_1!\langle \mu L\rangle.\ p_1?(\mu L) \ldots$. Global types capture the structure of the parallelisation, which can in some cases be used to justify the achieved speedups. For example, we can observe that the mergesort global type contains a part that needs to happen sequentially (p_0 and the last merging point in p_1), and this will prevent us from achieving linear speedups.

7 Related Work

López et al. [50] develop a verification framework for MPI/C inspired by MPST by translating parameterised protocol specifications to protocols in VCC [19]. They focus on verification, not on code or protocol generation. Ng et al. [59; 60] use parameterised MPST [25] to generate an MPI backbone in C that encapsulates the *whole* protocol (i.e., every endpoint), and merges it with user-supplied computation kernels. Several authors (e.g. [10]) generate skeleton API from extensions of Scribble (www.scribble.org). Their approach requires the protocol to be specified beforehand, and it is not extracted from sequential code. Unlike ours, none of the above work formally defines code generation or proves its correctness.

Structured parallelism includes the use of high-level constructs in languages with implicit/data parallelism [5; 12–15; 46; 64], algorithmic skeleton APIs [1; 18; 20; 36; 48], and DSLs/APIs that compile to parallel code [8; 11; 28; 63; 69]. Besides safety, such approaches are often highly optimised.

However, most rely on using a fixed, predetermined range of patterns, typically by design with respect to their application domains. By contrast, our work only relies on send/receive operations, which makes it highly portable, and can be easily extended to support further parallel structures by extending the annotation strategies. Optimisations for structured parallel approaches also require to study and define a set of equivalences between patterns [6; 7; 41]. In contrast, our approach does not require the definition of new sets of equivalences, since these are derived from program equivalences. *Lift* is a new language for portable parallel code generation, based on a small set of expressive parallel primitives [40; 67; 68]. Currently, their backend focuses on generating high-performance OpenCL code, while our approach focuses on placing computations on different participants of a concurrent/distributed system. Both approaches could be combined: annotations can be used to generate a high-level message-passing layer that distributes tasks to multiple nodes in a GPU cluster, using the global type to minimise communication costs; then, the code at each participant can be compiled to high-performance GPU code using Lift.

Elliott exploits the idea of giving functional programs multiple interpretations in different categories, and shows examples of applications to multiple domains, including parallelism [29; 30]. Our approach is similar in the sense that we allow the specifications of first-order functional programs to have multiple different interpretations, but we focus on generating parallel code, and provide a finer-grained control over the parallelisations by adding participant annotations. There is a large body of literature in using program equivalences to derive parallel implementations, e.g. [17; 32; 37; 39; 49; 51; 55; 56; 65; 66]. Our framework is orthogonal, in that we focus on tying a low-level C back-end with global types. Our front-end, however, supports some basic form of rewritings, and we plan to extend it in the future with more interesting ones from the literature.

8 Conclusions and Future Work

We have presented a novel approach to protocol inference and code generation. By using this approach, we can reason about *extensionality* of the parallel programs, and alternative mappings of computations to *participants*. We produce the parallel program global type, i.e. its communication protocol, that acts as a contract for the low-level code, can be used to pin-point potential optimisations, or assessing the suitability of a parallelisation. This approach has several benefits: 1. our message-passing code is deadlock-free by construction, since it follows the data-flow of the program, and the optimisations must respect the global type; 2. we prove that our parallelisations are extensionally equivalent to the input function. Additionally, PAlg code could be used for further multiple purposes, such as parallel GPU/FPGA code generation, by combining our approach with other state of the art code generation techniques. We will study this for future work.

Though our approach can already generate representative parallel protocols, our framework is extensible. E.g. we can extend our framework with dynamic participants to handle dynamic task generation [26], and we plan to use this to capture a wider range of communication patterns for parallel computing, such as load-balancing or work-stealing. We plan to study the extension of our back-end to heterogeneous architectures, e.g. GPU clusters, or FPGAs. Our prototype generates code that can achieve speedups against sequential implementations, the optimisations that we support are very basic, and our generated code can be very large. We plan to introduce optimisations that reduce the amount messages exchanged, further message reorderings guided by the global type, and optimisations of the size of the generated code. Finally, we plan to study the instrumentation of global types to estimate statically the speedups of different parallelisations, and optimise communication costs.

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A Further Definitions

A.1 Algebraic Functional Language

$$
\begin{aligned}
F_1, F_2 &::= \mathsf{I} \mid \mathsf{K}a \mid F_1 + F_2 \mid F_1 \times F_2 \\
a, b &::= 1 \mid \mathsf{int} \mid \ldots \mid a \to b \mid a + b \mid a \times b \mid F\ a \mid \mu F \\
e_1, e_2 &::= f \mid v \mid \mathsf{const}\ e \mid \mathsf{id} \mid e_1 \circ e_2 \mid \pi_i \mid e_1 \vartriangle e_2 \mid \iota_i \mid e_1 \triangledown e_2 \\
&\quad \mid F\ e \mid \mathsf{in}_F \mid \mathsf{out}_F \mid \mathsf{rec}_F\ e_1\ e_2
\end{aligned}
$$

$$
\frac{f : a \to b \in \Gamma}{\vdash f : a \to b} \qquad \frac{\vdash e : a}{\vdash \mathsf{const}\ e : b \to a} \qquad \frac{}{\vdash \mathsf{id} : a \to a}
$$

$$
\frac{}{\vdash \mathsf{in}_F : F\ \mu F \to \mu F} \qquad \frac{}{\vdash \mathsf{out}_F : \mu F \to F\ \mu F}
$$

$$
\frac{\vdash e_1 : b \to c \qquad \vdash e_2 : a \to b}{\vdash e_1 \circ e_2 : a \to c} \qquad \frac{i \in [1, 2]}{\vdash \pi_i : a_1 \times a_2 \to a_i}
$$

$$
\frac{\vdash e_1 : a \to b \qquad \vdash e_2 : a \to c}{\vdash e_1 \vartriangle e_2 : a \to b \times c} \qquad \frac{i \in [1, 2]}{\vdash \iota_i : a_i \to a_1 + a_2}
$$

$$
\frac{\vdash e_1 : a \to c \qquad \vdash e_2 : b \to c}{\vdash e_1 \triangledown e_2 : a + b \to c} \qquad \frac{\vdash e : a \to b}{\vdash F\ e : F\ a \to F\ b}
$$

$$
\frac{\vdash e_1 : F\ b \to b \qquad \vdash e_2 : a \to F\ a}{\vdash \mathsf{rec}_F\ e_1\ e_2 : a \to b}
$$

Figure 7. Syntax and types of Alg.

A.1.1 Properties of Alg Constructs

Alg constructs are characterised by well-known properties. Sum and product functions, are uniquely determined by their universal properties. Composition and identity must satisfy the associativity and cancellation properties. These basic properties are summarised in Fig. 9. Functors preserve identities and composition, and rec satisfy the *hylomorphism laws* (Fig. 9b). The laws of hylomorphisms can be used to perform some common program optimisations. For example, the well-known *deforestation* transformation can be

Constant, Identity and Composition

$$\mathsf{const}\ e = \lambda x.\ e \qquad \mathsf{id} = \lambda x.\ x \qquad e_1 \circ e_2 = \lambda x.\ e_1\ (e_2\ x)$$

Products

$$\pi_i = \lambda(x_1, x_2).\ x_i,\ i \in [1,2] \qquad e_1 \vartriangle e_2 = \lambda x.\ (e_1\ x,\ e_2\ x)$$

$$e_1 \times e_2 = (e_1 \circ \pi_1) \vartriangle (e_2 \circ \pi_2)$$

Coproducts

$$\iota_i = \lambda x.\ inj_i\ x \qquad e_1 \triangledown e_2 = \lambda(inj_i\ x).\ e_i\ x$$

$$e_1 + e_2 = (\iota_1 \circ e_1) \triangledown (\iota_2 \circ e_2)$$

Functors

$$\mathsf{I}\ a = a \qquad \mathsf{K}a\ b = a \qquad (F_1 \dagger F_2)\ a = F_1\ a \dagger F_2\ a, \quad \dagger \in \{+, \times\}$$

$$\mathsf{I}\ e = e \qquad \mathsf{K}a\ e = \mathsf{id} \qquad (F_1 \dagger F_2)\ e = F_1\ e \dagger F_2\ e$$

Recursion

$$\mathsf{in}_F = \lambda x.\ in_F\ x \qquad \mathsf{out}_F = \lambda(in_F\ x).\ x$$

$$\mathsf{rec}_F\ e_1\ e_2 = f \quad \text{where } f = e_1 \circ F\ f \circ e_2$$

Figure 8. Semantics of Alg combinators.

derived from the hylomorphism equation, and the properties of functors. Particularly, it is an instance of Equations A.11 and A.7 in Fig. 9. From the universal properties of $\vartriangle$ and $\triangledown$ (Fig. 9a), a number of equivalences can be derived, e.g.: $\pi_i \circ e_1 \vartriangle e_2 = e_i$; $(\pi_1 \circ e) \vartriangle (\pi_2 \circ e) = e$; $\pi_1 \vartriangle \pi_2 = \mathsf{id}$; $(e_1 \times e_2) \circ (e_3 \vartriangle e_4) = (e_1 \circ e_3) \vartriangle (e_2 \circ e_4)$; $e_1 \triangledown e_2 \circ \iota_i = e_i$; $(e \circ \iota_1) \triangledown (e \circ \iota_2) = e\iota_1 \triangledown \iota_2 = \mathsf{id}$; and $(e_1 \triangledown e_2) \circ (e_3 + e_4) = (e_1 \circ e_3) \triangledown (e_2 \circ e_4)$ where $i \in \{1, 2\}$. The properties of combinators provide a formal framework for equational reasoning that can be used as a basis for doing program transformations [31; 34; 53]. These properties have been used for parallelising functions, e.g. [17; 33; 55]. In this paper, we use $=_{\text{ext}}$ for the equations in 9, to distinguish them from the syntactic equality (=).

A.2 Parallel Algebraic Language

A.2.1 Typing rules

Rules Id, Comp, Proj_i and Split are standard. The main feature of this type system is the use of eta-expanded sum-types and unions of interfaces to deal with choices. Rule Choice specifies that a choice point may be introduced at any point when a participant contains a value of a sum-type. In such cases p sends the tag of the sum-type value to any other participant whose behaviour depends on it. After the choice point, the interface is $I[\iota_1\ \mathsf{p}] \cup^{\vec{\mathsf{p}}} I[\iota_2\ \mathsf{p}]$, with the constraint that the participants in $\mathcal{I}[\mathsf{p}]$ must be in $\vec{\mathsf{p}}$. Rule Alt specifies that e must be the parallelisation of e, considering both A_1 and A_2 as input interfaces. The output interface is the union of B_1 and B_2. Any participant in e must be notified of the choice $\mathsf{pids}(\mathsf{e}) \subseteq \vec{\mathsf{p}}$, to make sure that they perform the interactions that correspond to the correct A_i. Rule Join is the same as rule Alt, but we do not require the participants in e to be notified of the choice, since the input interface is the same in both branches of the choice. Rule Inj_i is used to *tag* an interface with the i-th injection. Then, rule Case_i specifies that if e_i is the parallelisation of e_i, then $\mathsf{e}_1 \triangledown \mathsf{e}_2$ is a parallelisation of $e_1 \triangledown e_2$, given the tagged input interface $\iota_i\ I_a$. Note that e_j with $j \neq i$ is free in rule Case_i. We solve these cases by unification (see rule Alt). If this is not possible, this means that no branch in the control flow will ever reach e_j, and so we can set it to any arbitrary parallelisation of e_j, or even optimise e_j away. Rule Alg specifies that given any e and participant p, e@p is a valid parallelisation, with output interface b@p. Finally, rule Ext is crucial for exploring potential parallelisations. It states that if e is the parallelisation of e_2, and e_2 is extensionally equal to e_1, then e is also a parallelisation of e_1. The undecidability of this rule requires that the programmer specifies *rewriting strategies* both for checking and inference.

Example A.1 (Rewriting and annotation strategies)**.** We illustrate how rewriting and annotation strategies work by showing the mergesort (**ms**) example. Consider the mergesort definition $\mathbf{ms} = \mathsf{rec}_T\ \mathsf{mrg}\ \mathsf{spl}$. Solutions to the inference problem $\vdash \mathbf{ms} \Rightarrow \mathbf{?0} : \mathsf{Ls@p}_0 \longrightarrow \mathbf{?1}$ provide the alternative parallelisations of **ms**. The only two rules that can be applied are Alg or Ext. By rule Alg, we can annotate **ms** at some p_1: $\vdash \mathbf{ms} \Rightarrow \mathbf{ms@p}_1 : \mathsf{Ls@p}_0 \longrightarrow \mathsf{Ls@p}_1$. Alternatively, we can use the hylomorphism equation, and apply rule Ext:

$$\mathbf{ms} = \mathsf{rec}_T\ \mathsf{mrg}\ \mathsf{spl} = \mathsf{mrg} \circ T\ \mathbf{ms} \circ \mathsf{spl} = \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms}) \circ \mathsf{spl}$$

We decide which of the rules to apply by querying a collection of rewriting hints, that we call *rewriting strategy*. This collection of hints is of the form $[e_1 : \mathsf{rw}_1, \ldots]$, and must be specified by the programmer. The rewritings rw_i are essentially proofs that $e_i =_{\text{ext}} e_i'$, by applying equations in Fig. 9. Once a hint is used, it is removed from the collection of hints. For the mergesort example, if we use the rewriting strategy [**ms** : `unroll 1`], we will apply rule Ext, unroll the hylomorphism equation once, and continue with an empty strategy [].

$$\frac{\vdash \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms}) \circ \mathsf{spl} \Rightarrow \mathbf{?0} : \mathsf{Ls@p}_0 \longrightarrow \mathbf{?1} \qquad \mathbf{ms} =_{\text{ext}} \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms}) \circ \mathsf{spl}}{\vdash \mathbf{ms} \Rightarrow \mathbf{?0} : \mathsf{Ls@p}_0 \longrightarrow \mathbf{?1}}$$

With an empty rewriting strategy, the only possibility once we find the atomic function spl is to use rule Alg. To select a participant, we query the *annotation* strategy. The annotation strategy is a collection of expressions that we require to place at distinct participants. Suppose that our annotation strategy is {spl}. Then, we would need to select a fresh participant p_1: $\vdash \mathsf{spl} \Rightarrow \mathsf{spl@p}_1 : \mathsf{Ls@p}_0 \longrightarrow ((1 + a) + \mathsf{Ls} \times \mathsf{Ls})\mathsf{@p}_1$. If the annotation strategy does not contain spl, then we would select any participant from the input interface, to minimise the amount of messages exchanged: $\vdash \mathsf{spl} \Rightarrow \mathsf{spl@p}_0 : \mathsf{Ls@p}_0 \longrightarrow ((1 + a) + \mathsf{Ls} \times \mathsf{Ls})\mathsf{@p}_0$. Suppose that the annotation strategy is {spl}. Then, after spl we have a sum type at p_1. This requires us to introduce a choice point:

$$\mathsf{id} \circ e = e \circ \mathsf{id} = e \quad \text{(A.1)} \qquad e_1 \circ (e_2 \circ e_3) = (e_1 \circ e_2) \circ e_3 \quad \text{(A.4)}$$

$$F\ (e_1 \circ e_2) = F\ e_1 \circ F\ e_2 \quad \text{(A.2)} \qquad f = e_1 \vartriangle e_2 \Leftrightarrow \pi_1 \circ e = e_1 \ \wedge\ \pi_2 \circ e = e_2 \quad \text{(A.5)}$$

$$F\ \mathsf{id} = \mathsf{id} \quad \text{(A.3)} \qquad e = e_1 \triangledown e_2 \Leftrightarrow e \circ \iota_1 = e_1 \ \wedge\ e \circ \iota_2 = e_2 \quad \text{(A.6)}$$

(a) Basic Properties of Combinators

$$e_3 \circ e_4 = \mathsf{id} \ \Rightarrow\ (\mathsf{rec}_F\ e_1\ e_3) \circ (\mathsf{rec}_F\ e_4\ e_2) = \mathsf{rec}_F\ e_1\ e_2 \quad \text{(A.7)} \qquad \mathsf{rec}_F\ \mathsf{in}\ \mathsf{out} = \mathsf{id}_{\mu F} \quad \text{(A.9)}$$

$$\eta : F_1 \to F_2 \ \Rightarrow\ \mathsf{rec}_{F_1}\ (e_1 \circ \eta)\ e_2 = \mathsf{rec}_{F_2}\ e_1\ (\eta \circ e_2) \quad \text{(A.8)} \qquad e_1, e_2 \text{ strict} \ \Rightarrow \mathsf{rec}_F\ e_1\ e_2 \text{ strict} \quad \text{(A.10)}$$

$$e_1 \text{ strict} \ \wedge e_1 \circ e_3 = e_5 \circ F\ e_1 \wedge e_4 \circ e_2 = F\ e_2 \circ e_6 \quad \Rightarrow \quad e_1 \circ (\mathsf{rec}_F\ e_3\ e_4) \circ e_2 = \mathsf{rec}_F\ e_5\ e_6 \quad \text{(A.11)}$$

(b) Hylomorphism Laws

Figure 9. Properties of point-free combinators

$$\frac{\vdash \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms}) \Rightarrow ?0 \qquad \{\mathsf{p}_1\} \subseteq ?3 \qquad : ((1+a) + \mathsf{Ls} \times \mathsf{Ls})@(\iota_1\ \mathsf{p}_1 \cup^{?3} \iota_2\ \mathsf{p}_1) \to ?1}{\vdash \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms}) \Rightarrow ?0 \circ [\mathsf{p}_1 \oplus ?3] : ((1+a) + \mathsf{Ls} \times \mathsf{Ls})@\mathsf{p}_1 \to ?1 \cup^{?3} ?2}$$

By collecting all constraints of the form $\{\mathsf{p}_1\} \subseteq ?3$, we can fully determine what is the minimum list of participants $?3$ that we require. To conclude our example, we show the end result of applying the rest of the rules. The final structure follows a divide-and-conquer parallel structure, that may bypass p_2 and p_3 if the input list is empty or singleton.

$$\begin{aligned}&\vdash \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms})\\ &\quad \Rightarrow \mathsf{mrg}@\mathsf{p}_1 \circ (\mathsf{id} + (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1))\\ &\qquad \circ [\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \circ \mathsf{spl}@\mathsf{p}_1\\ &\quad : \mathsf{Ls}@\mathsf{p}_0 \to \mathsf{Ls}@\mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \mathsf{Ls}@\mathsf{p}_1\end{aligned}$$

Definition A.2 (Product and Injection of Choice Interfaces)**.** We sometimes write $A \times B$ to represent the product of interfaces that contain choices. We do the product of the respective interfaces, after performing first the choices in A, and then the choices in B:

$$(R_1 \cup^{\vec{\mathsf{p}}} R_2) \times R_3 = (R_1 \times R_3) \cup^{\vec{\mathsf{p}}} (R_2 \times R_3)$$
$$I_1 \times (R_1 \cup^{\vec{\mathsf{p}}} R_2) = (I_1 \times R_1) \cup^{\vec{\mathsf{p}}} (I_1 \times R_2)$$

We also write injections of interfaces that contain choices:

$$\iota_i\ (R_1 \cup^{\vec{\mathsf{p}}} R_2) = \iota_i\ R_1 \cup^{\vec{\mathsf{p}}} \iota_i\ R_2$$

Definition A.3 (Well-formedness of interfaces: $\mathsf{WF}(a@R)$)**.** Interface $a@R$ is well formed if R matches the structure of a:

$$\frac{}{\mathsf{WF}(a@\mathsf{p})} \qquad \frac{\mathsf{WF}(a_i@I)}{\mathsf{WF}((a_1 + a_2)@(\iota_i\ I))} \qquad \frac{\mathsf{WF}(a@I_1) \qquad \mathsf{WF}(b@I_2)}{\mathsf{WF}((a \times b)@(I_1 \times I_2))}$$

$$\frac{\mathsf{WF}(a@R_1) \qquad \mathsf{WF}(a@R_2)}{\mathsf{WF}(a@(R_1 \cup^{\vec{\mathsf{p}}} R_2))}$$

For well-formed interfaces, we sometimes propagate the annotation down the type structure, e.g. $a@R_1 \times b@R_2$ is notation for $(a \times b)@(R_1 \times R_2)$. We also define sums of interfaces $a@R_1 +^{\vec{\mathsf{p}}} b@R_2$ as notation for $(a + b)@(\iota_1\ R_1 \cup^{\vec{\mathsf{p}}} \iota_2\ R_2)$.

$$\textsc{Join}\ \frac{\vdash e \Rightarrow \mathsf{e} : A \to B}{\vdash e \Rightarrow \mathsf{e} : A \cup^{\vec{\mathsf{p}}} A \to B \cup^{\vec{\mathsf{p}}} B}$$

$$\textsc{Alt}\ \frac{\vdash e \Rightarrow \mathsf{e} : A_1 \to B_1 \qquad \vdash e \Rightarrow \mathsf{e} : A_2 \to B_2 \qquad A_1 \neq A_2 \qquad \mathsf{pids}(\mathsf{e}) \subseteq \vec{r}}{\vdash e \Rightarrow \mathsf{e} : A_1 \cup^{\vec{\mathsf{p}}} A_2 \to B_1 \cup^{\vec{\mathsf{p}}} B_2}$$

$$\textsc{Alg}\ \frac{\vdash e : a \to b}{\vdash e \Rightarrow e@\mathsf{p} : a@I \to b@\mathsf{p}} \qquad \textsc{Ext}\ \frac{\vdash e_2 \Rightarrow \mathsf{e} : a@I \to B \qquad e_1 =_{\mathsf{ext}} e_2}{\vdash e_1 \Rightarrow \mathsf{e} : a@I \to B}$$

$$\textsc{Inj}_i\ \frac{}{\vdash \iota_i \Rightarrow \iota_i : a@I \to a@(\iota_i\ I)} \qquad \textsc{Id}\ \frac{}{\vdash \mathsf{id} \Rightarrow \mathsf{id} : A \to A}$$

$$\textsc{Proj}_i\ \frac{i \in [1,2]}{\vdash \pi_i \Rightarrow \pi_i : (a_1 \times a_2)@(I_1 \times I_2) \to a_i@I_i}$$

$$\textsc{Comp}\ \frac{\vdash e_1 \Rightarrow \mathsf{e}_1 : B \to C \qquad \vdash e_2 \Rightarrow \mathsf{e}_2 : A \to B}{\vdash e_1 \circ e_2 \Rightarrow \mathsf{e}_1 \circ \mathsf{e}_2 : A \to C}$$

$$\textsc{Case}_i\ \frac{\vdash e_i \Rightarrow \mathsf{e}_i : a_i@I \to B}{\vdash e_1 \triangledown e_2 \Rightarrow \mathsf{e}_1 \triangledown \mathsf{e}_2 : (a_1 + a_2)@(\iota_i\ I) \to B}$$

$$\textsc{Split}\ \frac{\vdash e_1 \Rightarrow \mathsf{e}_1 : a@I \to B \qquad \vdash e_2 \Rightarrow \mathsf{e}_2 : a@I \to C}{\vdash e_1 \vartriangle e_2 \Rightarrow \mathsf{e}_1 \vartriangle \mathsf{e}_2 : a@I \to B \times C}$$

$$\textsc{Choice}\ \frac{\vdash e \Rightarrow \mathsf{e} : a@(\mathcal{I}[\iota_1\ \mathsf{p}] \cup^{\vec{\mathsf{p}}} \mathcal{I}[\iota_2\ \mathsf{p}]) \to B \qquad \mathsf{pids}(\mathcal{I}[\mathsf{p}]) \subseteq \vec{\mathsf{p}} \qquad \mathsf{tyAt}(\mathcal{I}, a) = b + c}{\vdash e \Rightarrow \mathsf{e} \circ [\mathsf{p} \oplus \vec{\mathsf{p}}] : a@\mathcal{I}[\mathsf{p}] \to B}$$

Figure 10. Typing rules of PAlg

Definition A.4 (Projection Rules $(G \upharpoonright \mathsf{p})$ and Merging $(L_1 \sqcap L_2)$)**.** Projection defines how to obtain the local type L of a participant p in a global type G: $G \upharpoonright \mathsf{p}$.

$$(\mathsf{p}_1 \to \mathsf{p}_2 : a.\ G) \upharpoonright \mathsf{p} = \begin{cases} \mathsf{p}_2!\langle a\rangle.\ (G \upharpoonright \mathsf{p}) & \text{if } \mathsf{p} = \mathsf{p}_1 \neq \mathsf{p}_2 \\ \mathsf{p}_1?(a).\ (G \upharpoonright \mathsf{p}) & \text{if } \mathsf{p} = \mathsf{p}_2 \neq \mathsf{p}_1 \\ G \upharpoonright \mathsf{p} & \text{otherwise} \end{cases}$$

$$(\mathsf{p}_1 \to \mathsf{p}_2 : \{l_i.G_i\}_{i\in I}) \upharpoonright \mathsf{p} = \begin{cases} \mathsf{p}_2 \oplus \{l_i.G_i \upharpoonright \mathsf{p}\} & \text{if } \mathsf{p} = \mathsf{p}_1 \\ \mathsf{p}_1\ \&\ \{l_i.G_i \upharpoonright \mathsf{p}\} & \text{if } \mathsf{p} = \mathsf{p}_2 \\ \sqcap_{i\in I}(G_i \upharpoonright \mathsf{p}) & \text{otherwise} \end{cases}$$

$$\mu X.(G \upharpoonright \mathsf{p}) \quad \text{if } G \upharpoonright \mathsf{p} \neq X',\ \forall X'$$

$$\mathsf{p} \,\&\, \{l_i.L_i\}_{i\in I} \sqcap \mathsf{p} \,\&\, \{l_j.L'_j\}_{j\in J} = \mathsf{p} \,\&\, \{l_k.L_k \sqcap L'_k\}_{k\in I\cap J} \cup \{l_l.L_l\}_{l\in I\setminus J} \cup \{l_m.L_m\}_{m\in J\setminus I}$$

$$\mu X.L_1 \sqcap \mu X.L_2 = \mu X.(L_1 \sqcap L_2) \qquad L \sqcap L = L$$

Projection onto a role is not necessarily defined. Particularly, projecting $\mathsf{p}_1 \to \mathsf{p}_2 : \{l_i.G_i\}$ onto p, with $\mathsf{p} \neq \mathsf{p}_1$ and $\mathsf{p} \neq \mathsf{p}_2$, is only defined if the projection of all G_i onto p can be *merged*. Two local types can be merged only if they are the same, or if they branch on the same role p, and their continuations can be merged. For example, p_3's local type of the global type: $\mu X.\mathsf{p}_1 \to \mathsf{p}_2\{l_1.\mathsf{p}_2 \to \mathsf{p}_3 : l_2.\mathsf{p}_1 \to \mathsf{p}_3 : l_3.X,\ l_4.\mathsf{p}_2 \to \mathsf{p}_3 : l_5.\mathsf{p}_1 \to \mathsf{p}_3 : l_6.\mathsf{end}\}$ is $\mu X.\mathsf{p}_2 \,\&\, \{l_2.\mathsf{p}_1 \,\&\, \{l_3.X\}, l_4.\mathsf{p}_2 \,\&\, \{l_5.\mathsf{p}_1 \,\&\, \{l_6.\mathsf{end}\}\}\}$.

Definition A.5. LTS for Local Type Configurations

$$\boxed{\langle C, Q\rangle \rightsquigarrow^{\ell} \langle C', Q'\rangle} \quad C = [\mathsf{p}_i \mapsto L_i]_{i\in I} \quad Q = [\mathsf{p}_i\mathsf{p}_j \mapsto w]_{i\in I, j\in I}$$

$$\langle C[\mathsf{p}_0 \mapsto \mathsf{p}_1!\langle a\rangle.\ L], Q[\mathsf{p}_0\mathsf{p}_1 \mapsto w]\rangle \to^{\mathsf{p}_0\mathsf{p}_1!\langle a\rangle} \langle C[\mathsf{p}_0 \mapsto L], Q[\mathsf{p}_0\mathsf{p}_1 \mapsto a \cdot w]\rangle$$

$$\langle C[\mathsf{p}_0 \mapsto \mathsf{p}_1?(a).\ L], Q[\mathsf{p}_1\mathsf{p}_0 \mapsto w \cdot a]\rangle \to^{\mathsf{p}_0\mathsf{p}_1?(a)} \langle C[\mathsf{p}_0 \mapsto L], Q[\mathsf{p}_1\mathsf{p}_0 \mapsto w]\rangle$$

$$\langle C[\mathsf{p}_0 \mapsto \mathsf{p}_1 \oplus \{\iota_i.L_i\}_{i\in I}], Q[\mathsf{p}_0\mathsf{p}_1 \mapsto w]\rangle \to^{\mathsf{p}_0\mathsf{p}_1\oplus l_i} \langle C[\mathsf{p}_0 \mapsto L_i], Q[\mathsf{p}_0\mathsf{p}_1 \mapsto l_i \cdot w]\rangle$$

$$\langle C[\mathsf{p}_0 \mapsto \mathsf{p}_1 \,\&\, \{\iota_i.L_i\}_{i\in I}], Q[\mathsf{p}_1\mathsf{p}_0 \mapsto w \cdot l_i]\rangle \to^{\mathsf{p}_0\mathsf{p}_1\&l_i} \langle C[\mathsf{p}_0 \mapsto L_i], Q[\mathsf{p}_1\mathsf{p}_0 \mapsto w]\rangle$$

Definition A.6 (Interface projection: $A \upharpoonright \mathsf{p}$)**.** The projection of interface A onto role p is the part of interface A that is located at p. We define the projection for $a@R$ inductively on the structure of R:

$$a@\mathsf{p}_0 \upharpoonright \mathsf{p}_1 = \{a,\ \text{if } \mathsf{p}_0 = \mathsf{p}_1; 1,\ \text{otherwise}\}$$
$$(a \times b)@(R_1 \times R_2) \upharpoonright \mathsf{p} = (a@R_1 \upharpoonright \mathsf{p}) \times (b@R_2 \upharpoonright \mathsf{p})$$
$$(a_1 + a_2)@(\iota_i\ R) \upharpoonright \mathsf{p} = (a_i@R \upharpoonright \mathsf{p})$$
$$a@(R_1 \cup^{\vec{\mathsf{p}}} R_2) \upharpoonright \mathsf{p} = \begin{cases} (a@R_1) \upharpoonright \mathsf{p} \uplus (a@R_2) \upharpoonright \mathsf{p} & \text{if } \mathsf{p} \in \vec{\mathsf{p}} \\ a' & \text{if } a' = (a@R_1) \upharpoonright \mathsf{p} = (a@R_2) \upharpoonright \mathsf{p} \end{cases}$$

A.3 MPST

Definition A.7 (Label Broadcasting)**.** We define a macro that represents the broadcasting of a label to multiple participants in a choice. We write

- $\mathsf{p} \to \{\mathsf{p}_j\}_{j\in[1,n]} : \{\iota_i.G_i\}_{i\in I}$ for $\mathsf{p} \to \mathsf{p}_1\{\iota_i.\mathsf{p} \to \mathsf{p}_2\{\iota_i \ldots \mathsf{p} \to \mathsf{p}_n\{\iota_i.G_i\} \ldots\}\}_{i\in I}$; and
- $\{\mathsf{p}_j\}_{j\in[1,n]} \oplus \{\iota_i.L_i\}_{i\in I}$ for $\mathsf{p}_1 \oplus \{\iota_i.\mathsf{p}_2 \oplus \{\iota_i. \ldots \mathsf{p}_n \oplus \{\iota_i.L_i\} \ldots\}\}_{i\in I}$.

It is straightforward to show that $(\mathsf{p}_1 \to \{\mathsf{p}_j\}_{j\in J} : \{\iota_i.G_i\}_{i\in I}) \upharpoonright \mathsf{p}_2 = \{\mathsf{p}_j\}_{j\in J} \oplus \{\iota_i.G_i \upharpoonright \mathsf{p}_2\}_{i\in I}$, if $\mathsf{p}_1 = \mathsf{p}_2$, and $(\mathsf{p}_1 \to \{\mathsf{p}_j\}_{j\in J} : \{\iota_i.G_i\}_{i\in I}) \upharpoonright \mathsf{p}_2 = \mathsf{p}_1 \,\&\, \{\iota_i.G_i \upharpoonright \mathsf{p}_2\}_{i\in I}$, if $\mathsf{p}_2 = \mathsf{p}_j$ for some $j \in J$.

The relation $\vDash p \Leftarrow A \sim G$ associates p and A with the global type G (Fig. 11).

Rules Id, $\textsc{Inj}_i$, $\textsc{Proj}_i$, and $\textsc{Case}_i$ are straightforward. Rule Comp associates two PAlg expressions with the *sequencing* of their respective global types, $G_1 \fatsemi G_2$. The sequencing produces the global type that results of performing first G_1, and then G_2, by taking into account branching and choices:

$$\begin{aligned} \mathsf{end} \fatsemi G &= G \\ (\mathsf{p}_1 \to \mathsf{p}_2 : a.G_1) \fatsemi G_2 &= \mathsf{p}_1 \to \mathsf{p}_2 : a.(G_1 \fatsemi G_2) \\ (\mathsf{p}_1 \to \mathsf{p}_2\{\iota_i.G_1\}) \fatsemi G_2 &= \mathsf{p}_1 \to \mathsf{p}_2\{\iota_i.G_1 \fatsemi G_2\} \\ (\mathsf{p}_1 \to \mathsf{p}_2 \begin{Bmatrix} \iota_1.G_1 \\ \iota_2.G_2 \end{Bmatrix}) \fatsemi (G_3 \cup G_4) &= \mathsf{p}_1 \to \mathsf{p}_2 \begin{Bmatrix} \iota_1.G_1 \fatsemi G_3 \\ \iota_2.G_2 \fatsemi G_4 \end{Bmatrix} \\ (G_1 \cup G_2) \fatsemi (G_3 \cup G_4) &= (G_1 \fatsemi G_3) \cup (G_2 \fatsemi G_4) \end{aligned}$$

Rule Choice turns a choice point of p with dependencies $\vec{\mathsf{p}}$ into a global type choice: p notifies all participants in $\vec{\mathsf{p}}$ of the branch in the protocol that they need to take. Rule Alt associates p with two protocols, G_1 and G_2, whenever the input interface is a choice of A_1 and A_2. Each G_i is the continuation that corresponds to the i-th branch of the choice that led to interface A_i. Note that contrary to the typing rules of Fig. 2, there is no rule Join. This is because Join was only used to avoid adding too many participants to a choice. However, at this point, these participants are known, and specified at the choice points. Rule Split associates a split of PAlg expressions with the sequence of the respective interactions. The rule uses $[G_2/\mathsf{end}]G_1$ instead of $\fatsemi$, because if G_1 contains a global type choice, then the interactions described by G_2 must be done after every branch in G_1. Since both G_1 and G_2 start from the same input interface, any choice in either G_i must be independent of any choice in the other G_j. Finally, rule Alg specifies that if the input interface is $a@I$, and the expression is $e@\mathsf{p}$, then the protocol comprises the sequence of interactions from all participants in I to participant p.

Example A.8 (Mergesort Protocol)**.** Recall the PAlg expression inferred for **ms** in Example A.1:

$$\begin{aligned} &\vdash \mathsf{mrg} \circ (\mathsf{id} + \mathbf{ms} \times \mathbf{ms}) \\ &\quad \Rightarrow \mathsf{mrg}@\mathsf{p}_1 \circ (\mathsf{id} + (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1)) \\ &\qquad \circ [\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \circ \mathsf{spl}@\mathsf{p}_1 \\ &\quad : \mathsf{Ls}@\mathsf{p}_0 \to \mathsf{Ls}@\mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \mathsf{Ls}@\mathsf{p}_1 \end{aligned}$$

We need to solve $\vDash \mathsf{mrg}@\mathsf{p}_1 \circ (\mathsf{id} + (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1)) \circ [\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \circ \mathsf{spl}@\mathsf{p}_1 \Leftarrow \mathsf{Ls}@\mathsf{p}_0 \sim \mathbf{?0}$.
The first step is a straightforward application of Comp. The case for $[\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3]$ is the result of applying rule Choice. To help readability, we use different colors for the left and the right branches:

$$\begin{aligned} &\vDash [\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \\ &\quad \Leftarrow ((1 + a) + \mathsf{Ls} \times \mathsf{Ls})@\mathsf{p}_1 \sim \mathsf{p}_1 \to \{\mathsf{p}_2\mathsf{p}_3\}\{\iota_1.\mathsf{end}; \iota_2.\mathsf{end}\} \end{aligned}$$

At this point, the input interface is:

$$((1 + a) + \mathsf{Ls} \times \mathsf{Ls})@(\iota_1\ \mathsf{p}_1) \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} ((1 + a) + \mathsf{Ls} \times \mathsf{Ls})@(\iota_2\ \mathsf{p}_1)$$

This means that we need to obtain two sub-protocols, for the left and the right branches respectively. The left branch is solved by applying rule $\textsc{Inj}_1$, while the right branch is solved by rules Case, $\textsc{Inj}_2$, Split, Comp and Alg:

$$\textsc{Id}\ \frac{}{\vDash \mathsf{id} \Leftarrow a@I \sim \mathsf{end}} \qquad \textsc{Inj}_i\ \frac{}{\vDash \iota_i \Leftarrow a_i@I \sim \mathsf{end}}$$

$$\textsc{Proj}_i\ \frac{i \in [1,2]}{\vDash \pi_i \Leftarrow (a_1 \times a_2)@(I_1 \times I_2) \sim \mathsf{end}}$$

$$\textsc{Choice}\ \frac{}{\vDash [\mathsf{p} \oplus \vec{\mathsf{p}}] \Leftarrow a[b+c]@I[\mathsf{p}] \sim \mathsf{p} \rightarrow \{\vec{\mathsf{p}} \setminus \mathsf{p}\}\{\iota_1.\ \mathsf{end};\ \iota_2.\ \mathsf{end}\}}$$

$$\textsc{Case}_i\ \frac{\vDash \mathsf{e}_i \Leftarrow a_i@I \sim G}{\vDash \mathsf{e}_1 \bigtriangledown \mathsf{e}_2 \Leftarrow (a_1 + a_2)@(\iota_i\ I) \sim G}$$

$$\textsc{Alt}\ \frac{\vDash \mathsf{e} \Leftarrow A_1 \sim G_1 \qquad \vDash \mathsf{e} \Leftarrow A_2 \sim G_2}{\vDash \mathsf{e} \Leftarrow A_1 \cup^{\vec{\mathsf{p}}} A_2 \sim G_1 \cup G_2}$$

$$\textsc{Alg}\ \frac{\vdash e : a \rightarrow b}{\vDash e@\mathsf{p} \Leftarrow a@I \sim [a@I \rightsquigarrow \mathsf{p}]}$$

$$\textsc{Comp}\ \frac{\vDash \mathsf{e}_2 \Leftarrow A \sim G_2 \qquad \vDash \mathsf{e}_1 \Leftarrow B \sim G_1 \qquad \vdash \mathsf{e}_2 : A \rightarrow B}{\vDash \mathsf{e}_1 \circ \mathsf{e}_2 \Leftarrow A \sim G_2 \fatsemi G_1}$$

$$\textsc{Split}\ \frac{\vDash \mathsf{e}_i \Leftarrow a@I \sim G_i \quad (i = 1,2)}{\vDash \mathsf{e}_1 \vartriangle \mathsf{e}_2 \Leftarrow a@I \sim [G_2/\mathsf{end}]G_1}$$

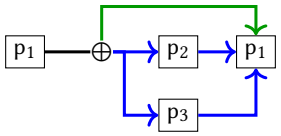
$[a@\mathsf{p}_1 \rightsquigarrow \mathsf{p}_2] = \mathsf{p}_1 \rightarrow \mathsf{p}_2 : a.\ \mathsf{end}$, if $\mathsf{p}_1 \neq \mathsf{p}_2$; $[a@\mathsf{p} \rightsquigarrow \mathsf{p}] = \mathsf{end}$;
$[(a \times b)@(I_a \times I_b) \rightsquigarrow \mathsf{p}] = [a@I_a \rightsquigarrow \mathsf{p}] \fatsemi [b@I_b \rightsquigarrow \mathsf{p}]$; and
$[(a_1 + a_2)@(\iota_i\ I) \rightsquigarrow \mathsf{p}] = [a_i@I \rightsquigarrow \mathsf{p}]$

Figure 11. Protocol relation

$$\begin{aligned}
&\vDash \mathsf{id} + (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1)\\
&\quad \Leftarrow ((1+a) + \mathsf{Ls} \times \mathsf{Ls})@(\iota_1\ \mathsf{p}_1) \sim \mathsf{end}\\
&\vDash \mathsf{id} + (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1)\\
&\quad \Leftarrow ((1+a) + \mathsf{Ls} \times \mathsf{Ls})@(\iota_2\ \mathsf{p}_1) \sim \mathsf{p}_1 \rightarrow \mathsf{p}_2 : \mathsf{Ls}.\mathsf{p}_1 \rightarrow \mathsf{p}_3 : \mathsf{Ls}.\mathsf{end}
\end{aligned}$$

The interface at this point is $((1+a) + \mathsf{Ls} \times \mathsf{Ls})@(\iota_1\ \mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \iota_2\ (\mathsf{p}_2 \times \mathsf{p}_3))$. For the last expression, $\mathsf{mrg}@\mathsf{p}_1$, we produce the following protocol: $\mathsf{end} \cup \mathsf{p}_2 \rightarrow \mathsf{p}_1 : \mathsf{Ls}.\mathsf{p}_3 \rightarrow \mathsf{p}_1 : \mathsf{Ls}.\mathsf{end}$. This is because, on the left branch, the input is still at p_1, so no communication is required. On the right branch, the input is a product of lists, one at p_2, and another one at p_3, and so they need to communicate with p_1. The final protocol is obtained by applying sequencing:

$$\begin{aligned}
&\mathsf{p}_1 \rightarrow \{\mathsf{p}_2\mathsf{p}_3\} \left\{ \begin{array}{l} \iota_1.\mathsf{end}; \\ \iota_2.\mathsf{p}_1 \rightarrow \mathsf{p}_2 : \mathsf{Ls}.\mathsf{p}_1 \rightarrow \mathsf{p}_3 : \mathsf{Ls}.\mathsf{end} \end{array} \right\} \fatsemi \\
&\qquad\qquad (\mathsf{end} \cup (\mathsf{p}_2 \rightarrow \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{p}_3 \rightarrow \mathsf{p}_1 : \mathsf{Ls}.\mathsf{end}))\\
&= \mathsf{p}_1 \rightarrow \{\mathsf{p}_2\mathsf{p}_3\} \left\{ \begin{array}{l} \iota_1.\mathsf{end}; \\ \iota_2.\mathsf{p}_1 \rightarrow \mathsf{p}_2 : \mathsf{Ls}.\mathsf{p}_1 \rightarrow \mathsf{p}_3 : \mathsf{Ls}. \\ \quad \mathsf{p}_2 \rightarrow \mathsf{p}_1 : \mathsf{Ls}.\ \mathsf{p}_3 \rightarrow \mathsf{p}_1 : \mathsf{Ls}.\mathsf{end} \end{array} \right\}
\end{aligned}$$



A.4 Mp

Mp comprises four basic operations: send, receive, choice and branching, with a standard (asynchronous) semantics. Additionally, for composing actions that depend on the same choice, we introduce case expressions.

$$\begin{aligned}
v &::= x \mid (v, v) \mid \iota_i\ v \mid \cdots \mid e\ v\\
m_i &::= \mathbf{ret}\ v \mid m \gg\!= f \mid \mathbf{send}\ \mathsf{p}\ v \mid \mathbf{recv}\ \mathsf{p}\ a \mid \mathbf{sel}\ \vec{\mathsf{p}}\ v\ f_1\ f_2\\
&\quad \mid \mathbf{brn}\ \mathsf{p}\ m_1\ m_2 \mid \mathbf{case}\ f_1\ f_2 \quad f ::= \lambda x.m
\end{aligned}$$

Values v are either primitive values, tagged values $\iota_i\ v$, pairs of values, or the result of applying an Alg expression e to a value. We use standard notation for the monadic unit (**ret**) and bind ($\gg\!=$). The term $\lambda x.\ m$ is a monadic continuation. We write $\lambda\_.\ m$ when the continuation discards the result of the previous monadic action. We use the standard Kleisli composition: $f_1 \gg\!= f_2 = \lambda x.\ f_1\ x \gg\!= f_2$.

The message-passing constructs are standard, except **sel**, **brn** and **case**, which are used for performing choices, and composing actions that depend on the same choice. We explain them in detail below. We include select and branching as syntactic constructs to simplify the typeability of parallel code against local types, but their semantics can be defined in terms of standard pattern matching, plus *send* and *receive* operations.

Each monadic computation f or m has a type m : Mp L a, where a is the return type of m, and L is the type index of Mp, and it represents the local type that corresponds to the behaviour of the term m. There is almost a one to one correspondence between the terms L and the monadic actions m, so we refer the reader to Appendix A (Fig. 16) for the full definition.

Composing Choices The types of the constructs that deal with choices use a new type, $\uplus$, that is isomorphic to sum types, but that can only be constructed and eliminated by using the following monadic constructs:

$$\begin{aligned}
&\mathbf{sel}\ \vec{\mathsf{p}} : a + b \rightarrow (a \rightarrow \mathrm{Mp}\ L_1\ c_1) \rightarrow (b \rightarrow \mathrm{Mp}\ L_2\ c_2)\\
&\qquad \rightarrow \mathrm{Mp}\ (\vec{\mathsf{p}} \oplus \{\iota_1.L_1; \iota_2.L_2\})\ (c_1 \uplus c_2)\\
&\mathbf{brn}\ \mathsf{p} : \mathrm{Mp}\ L_1\ a_1 \rightarrow \mathrm{Mp}\ L_2\ a_2\\
&\qquad \rightarrow \mathrm{Mp}\ (\mathsf{p}\ \&\ \{\iota_1.L_1; \iota_2.L_2\})\ (a_1 \uplus a_2)\\
&\mathbf{case} : (a \rightarrow \mathrm{Mp}\ L_1\ c) \rightarrow (b \rightarrow \mathrm{Mp}\ L_2\ d) \rightarrow a \uplus b\\
&\qquad \rightarrow \mathrm{Mp}\ (L_1 \cup L_2)\ (c \uplus d)
\end{aligned}$$

These constructs ensure that the tag used to build $a \uplus b$ indeed corresponds to the correct branch of the right choice. We use **case** to compose actions that depend on a previous choice. It may seem that this treatment of $\uplus$ leads to unnecessary code duplication, e.g. the only possibility to compose a single action f after a branch is using **case**: **brn** p m_1 $m_2 \gg\!=$ **case** f f. Our back-end easily optimises those cases to avoid code duplication.

By the definition of the monadic bind, when we compose a branch or select with a case expression, the final local type cannot contain $\cup$. To illustrate this, consider $m_1 : \mathsf{Mp}\ L_1\ (a \uplus b)$ and $f_2 : a \uplus b \to \mathsf{Mp}\ (L_2 \cup L_3)\ (c \uplus d)$. The local type of $m_1 \gg\!= f_2$ must be $L_1 \fatsemi (L_2 \cup L_3)$. But that is only defined if L_1 contains a branch or select. Therefore, $m_1 \gg\!= f_2$ is only well-typed if f_2 is a case expression on the tag introduced by the topmost branch or select of m_1.

Parallel programs We define the basic constructs of PAlg in a bottom-up way by manipulating *parallel programs*. Parallel programs are mappings from participants to their monadic action: $\mathsf{E} ::= [\mathsf{p}_i \mapsto m_i]_{i\in I}$. If $m_i : \mathsf{Mp}\ L_i\ a_i$ for all $i \in I$, then we write $[\mathsf{p}_i \mapsto m_i]_{i\in I} : \mathsf{Mp}\ [\mathsf{p}_i \mapsto L_i]_{i\in I}\ [\mathsf{p}_i \mapsto a_i]_{i\in I}$. The semantics of both local types and monadic actions is defined in terms of such collections of actions or local types, and shared queues of values W, or queues of types Q, e.g. $\langle \mathsf{E}, W\rangle \rightsquigarrow^{\ell} \langle \mathsf{E}', W'\rangle$ is a transition from E to E', and shared queues W to W' with *observable action* ℓ. We prove a standard safety theorem (Theorem 5.1 below) that guarantees that if a participant does a transition with some observable action, then so does the type index.

Theorem 5.1. *[Soundness] Assume* $\mathsf{E} : \mathsf{Mp}\ C\ A$, $m : \mathsf{Mp}\ L\ a$ *and* $W : Q$. *Suppose* $\langle \mathsf{E}[r \mapsto m], W\rangle \rightsquigarrow^{\ell} \langle \mathsf{E}[r \mapsto m'], W'\rangle$. *Then there exists* $\langle C[r \mapsto L], Q\rangle \to^{\ell} \langle C[r \mapsto L'], Q'\rangle$ *such that* $W' : Q'$ *and* $m' : \mathsf{Mp}\ L'\ a$.

Notations and Operations for Parallel Programs We simplify the notation for E, when all L_i are projections of the same global type, and the a_i are *projections of the same interface*. We define the projection of an interface at a participant, $A \upharpoonright \mathsf{p}$, to be the part of A that is at p (Appendix A.6). Whenever we have $m_{\mathsf{p}} : \mathsf{Mp}\ (G \upharpoonright \mathsf{p})\ (A \upharpoonright \mathsf{p})$ for all participants in $\mathsf{p} \in G$, we use the notation $[\mathsf{p} \mapsto m_{\mathsf{p}}]_{\mathsf{p}\in \mathsf{pids}(G)} : \mathsf{Mp}\ G\ A$. This means that the collection of all actions m_{p} *behave* as prescribed by G, and produce their result in interface A. Finally, if we have $\mathsf{E} = [\mathsf{p} \mapsto f_{\mathsf{p}} : A \upharpoonright \mathsf{p} \to \mathsf{Mp}\ (G \upharpoonright \mathsf{p})\ (B \upharpoonright \mathsf{p})]_{\mathsf{p}\in \mathsf{pids}(G)}$, we write $\mathsf{E} : A \to \mathsf{Mp}\ G\ B$.

Parallel programs have a default value for participants that are not in their domain. Unless otherwise specified, this default value is the identity. For example, $\mathsf{E}(\mathsf{p}) = f$ if $\mathsf{E} = \mathsf{E}'[\mathsf{p} \mapsto f]$, and $\mathsf{E}(\mathsf{p}) = \lambda x.\mathbf{ret}\ x$ if $\mathsf{p} \notin \mathsf{E}$. We specify the default value using the underscore character as a key in the mapping from participants to monadic actions: $[\_ \mapsto f]$.

Distributed Values and Execution We define the execution of a parallel program on a distributed value below. A distributed value $V : a@R$ is a mapping from participants to the value that they hold in the respective interface: $[\mathsf{p}_i \mapsto (v_i : (a@R) \upharpoonright \mathsf{p}_i)]_{i\in I} : (a@R)$. Additionally, we require unit to be the default value, so if $\mathsf{p} \notin \mathsf{pids}(R)$, then $V(\mathsf{p}) = ()$.

Definition A.9 (Execution)**.** Given $\mathsf{E} = [\mathsf{p}_i \mapsto f_i]_{i\in I}$ and $X = [\mathsf{p}_j \mapsto x_j]_{j\in J}$, we define $\mathsf{E}(X) = [\mathsf{p}_i \mapsto f_i\ X(\mathsf{p}_i)]_{i\in I}$, with $X(\mathsf{p}_i) = x_i$ if $i \in J$, or $X(\mathsf{p}_i) = ()$ otherwise. Given $Y = [\mathsf{p}_k \mapsto y_k]_{k\in K}$, we say that $P(X)$ executes to Y, $P(X) \rightsquigarrow^* Y$, if there is a trace $\langle P(X), \emptyset\rangle \rightsquigarrow^* \langle [\mathsf{p}_i \mapsto \mathbf{ret}\ Y(\mathsf{p}_i)]_{i\in I},\ \emptyset\rangle$. We write $P(X) = Y$, whenever there is a unique Y s.t. for all Z, $P(X) \rightsquigarrow^* Z$ implies that $Z = Y$.

Composition and Identity Composition is defined as the standard Kleisli composition, extended to parallel programs as follows: $\mathsf{E}_1 \gg\!=\!\gg \mathsf{E}_2 = [\mathsf{p} \mapsto \mathsf{E}_1(\mathsf{p}) \gg\!=\!\gg \mathsf{E}_2(\mathsf{p})]_{\mathsf{p}\in \mathsf{E}_1 \cup \mathsf{E}_2}$. Then, $\mathsf{E}_2 \circ \mathsf{E}_1 = \mathsf{E}_1 \gg\!=\!\gg \mathsf{E}_2$. Identity is simply the empty program with just the default value, $\mathsf{id} = []$.

Split and Projection The split operation is the participant-wise split, and the i-th projection is the environment with the projection i as the default value:

$$\mathsf{E}_1 \vartriangle \mathsf{E}_2 = [\mathsf{p} \mapsto \lambda x.\ \mathsf{E}_1(\mathsf{p})\ x \gg\!=\!\gg \lambda y.\ \mathsf{E}_2(\mathsf{p})\ x \gg\!=\!\gg \lambda z.\ \mathbf{ret}(y, z)]_{\mathsf{p}\in \mathsf{E}_1\cup\mathsf{E}_2}.$$
$$\pi_i = [\_ \mapsto \lambda x.\ \mathbf{ret}\ (\pi_i\ x)]$$

Case and Injection Case expressions will never occur during code generation, since they will be resolved by choices. Injections only *tag* a branch in the protocol, and so we define them as the identity: $\iota_i = []$.

Choices Choices are performed by the participant holding a value of a sum-type, and the tag is notified to the list of participants that depend on them. The definition uses functions $\mathsf{get}_{\mathcal{I}}(x)$ and $\mathsf{put}_{\mathcal{I}}(y, x)$ to extract the value of a sum-type from the hole of a one-hole context $\mathcal{I}$ (§3.1), and to replace the value at the hole respectively.

$$[\mathsf{p}_0 \oplus \mathsf{p}_0\mathsf{p}_1 \cdots \mathsf{p}_n] = \begin{bmatrix} \mathsf{p}_0 \mapsto \lambda x.\ \mathbf{sel}\ (\mathsf{p}_1 \cdots \mathsf{p}_n)\ (\mathsf{get}_{\mathcal{I}}(x)) \\ \qquad\qquad (\lambda y.\ \mathbf{ret}\ (\mathsf{put}_{\mathcal{I}}(y, x)))\ (\lambda y.\ \mathbf{ret}\ (\mathsf{put}_{\mathcal{I}}(y, x))) \\ \mathsf{p}_1 \mapsto \lambda x.\ \mathbf{brn}\ \mathsf{p}\ (\mathbf{ret}\ x)\ (\mathbf{ret}\ x); \\ \quad \cdots \\ \mathsf{p}_n \mapsto \lambda x.\ \mathbf{brn}\ \mathsf{p}\ (\mathbf{ret}\ x)\ (\mathbf{ret}\ x) \end{bmatrix}$$

The presence of type $\uplus$ means that we might require to perform a case expression to inspect the result of a previous choice: we define $\mathsf{E}_1 \uplus^{\vec{\mathsf{p}}} \mathsf{E}_2$ for this.

$$\mathsf{E}_1 \uplus^{\vec{\mathsf{p}}} \mathsf{E}_2 = [\mathsf{p} \mapsto \lambda x.\ \mathbf{case}\ \mathsf{E}_1(\mathsf{p})\ \mathsf{E}_2(\mathsf{p})]_{\mathsf{p}\in\vec{\mathsf{p}} \cup (\mathsf{E}_1 \cup \mathsf{E}_2) \setminus \{\vec{\mathsf{p}}\}}$$

The definition of $\uplus^{\vec{\mathsf{p}}}$ means that the participants involved in a choice will perform a case expression to inspect which branch they need to take, while the rest of the participants will continue as specified by either E_1 or E_2. Note $(\mathsf{E}_1 \cup \mathsf{E}_2)(\mathsf{p})$ will produce $\mathsf{E}_2(\mathsf{p})$, if $\mathsf{p} \in \mathsf{E}_1 \cap \mathsf{E}_2$. This will not be an issue during code generation: any participant that is not involved in a choice will have the same continuation in both branches.

A.5 Mp code generation

The translation scheme for Mp code generation (Fig. 12) is done recursively on the structure of PAlg expressions. It takes a PAlg expression e, an interface A, and produces a mapping from all participants in e and A to their respective monadic continuations. We write $[\![\mathsf{e}]\!]\,(A)$, and guarantee that $[\![\mathsf{e}]\!]\,(A) : A \to \mathsf{Mp}\ G\ B$, if $\vDash \mathsf{e} \Leftarrow A \sim (G, B)$. This means that if e induces protocol G with interfaces $A \to B$, then the generated code behaves as G, with interfaces A and B.

Code generation follows a similar structure to global type inference. For code generation, we require a partial function $\mathsf{cod}(\mathsf{e}, A)$ that infers the codomain interface of e using Fig. 2.

The translation to Mp requires to define the interactions from an interface I that gathers a type a at p: $(\!|a@I \rightsquigarrow \mathsf{p}|\!) : a@I \to \mathsf{Mp}\ [a@I \rightsquigarrow \mathsf{p}]\ (a@\mathsf{p})$. The definition is analogous to that of $[a@I \rightsquigarrow \mathsf{p}]$. The remaining of the translation is straightforward, built on top of the previous definitions.

$$
\begin{array}{ll}
(\!|a@\mathsf{p}_1 \rightsquigarrow \mathsf{p}_0|\!) & = [\mathsf{p}_1 \mapsto \lambda x.\mathbf{send}\ x\ \mathsf{p}_0 \mathsf{p}_0 \mapsto \lambda\_.\mathbf{recv}\ \mathsf{p}_1\ a] \\
(\!|(a_1 + a_2)@(\iota_i\ I) \rightsquigarrow \mathsf{p}|\!) & = (\!|a_i@I \rightsquigarrow \mathsf{p}|\!) \gg\!= [\mathsf{p} \mapsto \lambda x.\ \mathbf{ret}\ (\iota_i\ x)] \\
(\!|(a \times b)@(I_1 \times I_2) \rightsquigarrow \mathsf{p}|\!) & = (\!|a@I_1 \rightsquigarrow \mathsf{p}|\!) \times (\!|b@I_2 \rightsquigarrow \mathsf{p}|\!) \\
& \quad \gg\!= [\mathsf{p}_i \mapsto \lambda\_.\mathbf{ret}()]_{\mathsf{p}_i \in \mathsf{pids}(I_1 \times I_2) \setminus \{\mathsf{p}\}}
\end{array}
$$

$$
\begin{array}{ll}
[\![\mathsf{id}]\!]\ (a@I) & = [] \qquad [\![\iota_i]\!]\ (a@I) = [] \\
[\![e@\mathsf{p}]\!]\ (a@I) & = (\!|a@I \rightsquigarrow \mathsf{p}|\!) \gg\!= [\mathsf{p} \mapsto \lambda x.\ \mathbf{ret}\ (e\ x)] \\
[\![\mathsf{e}_1 \vartriangle \mathsf{e}_2]\!]\ (a@I) & = [\![\mathsf{e}_1]\!]\ (a@I) \vartriangle [\![\mathsf{e}_2]\!]\ (a@I) \\
[\![\mathsf{e}_1 \circ \mathsf{e}_2]\!]\ (A) & = [\![\mathsf{e}_2]\!]\ (A) \gg\!= [\![\mathsf{e}_1]\!]\ (\mathsf{cod}(\mathsf{e}_2, A)) \\
[\![\mathsf{e}_1 \triangledown \mathsf{e}_2]\!]\ ((a_1 + a_2)@(\iota_i\ I)) & = [\![\mathsf{e}_i]\!]\ (a_i@I) \\
[\![\pi_i]\!]\ ((a \times b)@(I_1 \times I_2)) & = \pi_i \\
[\![[\mathsf{p} \oplus \vec{\mathsf{p}}]]\!]\ (a@I\,[\mathsf{p}]) & = [\mathsf{p} \oplus \vec{\mathsf{p}}] \\
[\![\mathsf{e}]\!]\ (a@(R_1 \cup^{\vec{\mathsf{p}}} R_2)) & = [\![\mathsf{e}]\!]\ (a@R_1) \uplus^{\vec{\mathsf{p}}} [\![\mathsf{e}]\!]\ (a@R_2)
\end{array}
$$

Figure 12. Translating PAlg to Mp code.

Protocol Compliance Theorem 5.2 guarantees that the generated code follows the protocol inferred using the relation in Fig. 3. This fact is enough to guarantee that the generated code is deadlock-free. Moreover, we can use it to prove that the generated code is extensionally equal to the input Alg expression. We state this in Theorem 5.3.

Theorem 5.2. *[Protocol Conformance of the Generated Code] If* $\vDash \mathsf{e} \Leftarrow A \sim G$*, then* $[\![\mathsf{e}]\!]\ (A)$ *complies with protocol* G*.*

This theorem is proved by induction on the structure of the derivation $\vDash$, and by the definition of $\upharpoonright$. This result guarantees that the generated code corresponds to the protocol inferred from e. Since the protocol inferred from e is deadlock-free, then so is the generated code. See Appendix B.3.

Extensional Equivalence Additionally to deadlock-freedom and protocol compliance, we prove that if e is the annotation of e, then running the code generated from e on x produces the same result as evaluating e on x. This guarantees that, regardless of the annotations and interfaces chosen for e, *the parallel code always produces the same result as the sequential implementation.* We show the statement below, in Theorem 5.3, and refer to ***Appendix C*** for the full proof.

We specify the extensionality theorem on *runnable* parallel programs, which are those with a single entry/exit point, i.e. a *master* worker p_m that starts the computation, and gathers the results. Suppose we call this master worker p_m. Given any e, we can guarantee that p_m is the entry point by setting it to be the domain interface: $\vdash \mathsf{e} : a@\mathsf{p}_m \to b@R$. To make it the exit point, we need to make sure that it is the codomain interface. We can do this by forcing the participants in R to send their values to p_m as follows: $\vdash \mathsf{id}@\mathsf{p}_m \circ \mathsf{e} : a@\mathsf{p}_m \to b@R$. However, and due to the presence of choices, the codomain interface may contain $\cup$. For example, if $\mathsf{e} : a@\mathsf{p}_m \to b@(I_1 \cup^{\vec{\mathsf{p}}} I_2)$, with $I_1 \neq I_2$, then $\vdash \mathsf{id}@\mathsf{p}_m \circ \mathsf{e} : a@\mathsf{p}_m \to b@(\mathsf{p}_m \cup^{\vec{\mathsf{p}}} \mathsf{p}_m)$, with $\mathsf{p}_m \in \vec{\mathsf{p}}$. This means that $b@(\mathsf{p}_m \cup^{\vec{\mathsf{p}}} \mathsf{p}_m) \upharpoonright \mathsf{p}_m = b \uplus b$. To obtain a single value of type b, we use function $\mathsf{join} : a \uplus a \to a$, which is equivalent to $\mathsf{id} \triangledown \mathsf{id}$ for regular sum-types. We lift it to a monadic action, $\mathsf{join}(R)$, to join all branches in R:

$$\mathsf{join}(I) = [] \qquad \mathsf{join}(R_1 \cup^{\vec{\mathsf{p}}} R_2) = \mathsf{join}(R_1) \uplus^{\vec{\mathsf{p}}} \mathsf{join}(R_2) \gg\!= [\mathsf{p} \mapsto \lambda x.\ \mathbf{ret}\ (\mathsf{join}\ x)]_{\mathsf{p} \in \vec{\mathsf{p}}}$$

Note that $\mathsf{join}(R_1 \cup^{\vec{\mathsf{p}}} R_2)$ is only defined if $(a@R_1 \upharpoonright \mathsf{p}) = (a@R_2 \upharpoonright \mathsf{p})$ for all roles. The *runnable parallel program* for $\mathsf{e} : a@\mathsf{p}_m \to b@R$, $J[\![\mathsf{e}]\!]\ (\mathsf{p}_m)$, is defined as follows:

$$J[\![\mathsf{e}]\!]\ (\mathsf{p}_m) = [\![\mathsf{id}@\mathsf{p}_m \circ \mathsf{e}]\!]\ (a@\mathsf{p}_m) \gg\!= [\mathsf{p}_m \mapsto \mathsf{join}(R)].$$

Our extensionality statement specifies that executing the runnable parallel program for e, with master p and value x, produces value $e\ x$ at p.

Theorem 5.3. *[Extensionality] Assume* $e \Rightarrow \mathsf{e} : a@\mathsf{p} \to b@R$ *and* $x : a$ *initially at* p*. If* $e\ x = y$*, then the execution of* $[\![\mathsf{e}]\!]\ (\mathsf{p})$ *also produces* y*, distributed across* R*.*

Example A.10 (MergeSort Code Generation)**.** We start with the annotated **ms** from Example A.1, and we use p_1 as master role to avoid the initial communication from p_0 to p_1:

$$\mathsf{pms} = \mathsf{mrg}@\mathsf{p}_1 \circ (\mathsf{id} + (\mathbf{ms}@\mathsf{p}_2 \circ \pi_1@\mathsf{p}_1) \vartriangle (\mathbf{ms}@\mathsf{p}_3 \circ \pi_2@\mathsf{p}_1)) \circ [\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3] \circ \mathsf{spl}@\mathsf{p}_1 : \mathsf{Ls}@\mathsf{p}_1 \to \mathsf{Ls}@\mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \mathsf{Ls}@\mathsf{p}_1$$

Note that $[\![\mathsf{id}@\mathsf{p}_1 \circ \mathsf{pms}]\!]$ would be equivalent to $[\![\mathsf{pms}]\!]$ because the output interface is already of the form $\mathsf{p}_1 \cup \mathsf{p}_1$. Therefore, for simplicity, we show $[\![\mathsf{pms}]\!]$, and use it to produce the runnable parallel program. Fig. 14 show the code generation process using a table, where the i-th column is the current code for participant p_i, and the last column shows the expression and input interface that we are translating next.

From this point, we need to produce the code for the two branches. The left branch is straightforward, and is simply $\lambda x.\ \mathbf{ret}\ x$ for all participants. The result for the right branch is show in Fig. 15:

Next, we combine both branches using **case**, and compose it with the previous result. To avoid unnecessarily pattern-matching expressions such as **case**, we optimise the code using rules of the form:

$\mathbf{sel}\ \vec{\mathsf{p}}\ f_1\ f_2 \gg\!= \mathbf{case}\ f_3\ f_4 = \mathbf{sel}\ \vec{\mathsf{p}}\ (f_1 \gg\!= f_3)\ (f_2 \gg\!= f_4)$. Additionally, we optimise all instances of e.g. $(x, ())$ using the fact that $1 \times a \cong a \times 1 \cong a$. We show below the code for all p_i, after composing it with $\mathsf{join}(\mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \mathsf{p}_1)$, and applying these optimisations. We use different colours to highlight the different branches of the protocol:

$$
\begin{array}{l}
\mathsf{p}_1 \mapsto \textbf{sel}\ \{\mathsf{p}_2, \mathsf{p}_3\}\ (\text{spl}\ v)\ (\lambda x.\ \textbf{ret}\ (\text{mrg}\ (\iota_1\ x)))\ (\lambda x.\ \textbf{send}\ \mathsf{p}_2\ (\pi_1\ x) \gg\!= \lambda y.\ \textbf{send}\ \mathsf{p}_3\ (\pi_2\ x) \gg\!= \lambda\_.\\
\qquad \textbf{recv}\ \mathsf{p}_2\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{recv}\ \mathsf{p}_3\ \mathsf{Ls} \gg\!= \lambda y.\ \textbf{ret}\ (\text{mrg}\ (\iota_2\ (x, y)))) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_2 \mapsto \textbf{brn}\ \mathsf{p}_1\ \ (\textbf{ret}\ x)\ (\textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\ \textbf{send}\ \mathsf{p}_1\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_3 \mapsto \textbf{brn}\ \mathsf{p}_1\ \ (\textbf{ret}\ x)\ (\textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\ \textbf{send}\ \mathsf{p}_1\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\hline
\mathsf{p}_1 \mapsto \textbf{send}\ \mathsf{p}_2\ (\pi_1\ (v_1, v_2)) \gg\!= \lambda y.\ \textbf{send}\ \mathsf{p}_3\ (\pi_2\ (v_1, v_2)) \gg\!= \lambda\_.\ \textbf{recv}\ \mathsf{p}_2\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{recv}\ \mathsf{p}_3\ \mathsf{Ls} \gg\!= \lambda y.\\
\qquad \textbf{ret}\ (\text{br}_2\ (\text{mrg}\ (\iota_2\ (x, y)))) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_2 \mapsto \textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\ \textbf{send}\ \mathsf{p}_1\ x \gg\!= \lambda x.\ \textbf{ret}\ (\text{br}_2\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_3 \mapsto \textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\ \textbf{send}\ \mathsf{p}_1\ x \gg\!= \lambda x.\ \textbf{ret}\ (\text{br}_2\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\hline
\mathsf{p}_1 \mapsto \textbf{recv}\ \mathsf{p}_2\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{recv}\ \mathsf{p}_3\ \mathsf{Ls} \gg\!= \lambda y.\ \textbf{ret}\ (\text{br}_2\ (\text{mrg}\ (\iota_2\ (x, y)))) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_2 \mapsto \textbf{ret}\ (\textbf{ms}\ v_1) \gg\!= \lambda x.\ \textbf{send}\ \mathsf{p}_1\ x \gg\!= \lambda x.\ \textbf{ret}\ (\text{br}_2\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_3 \mapsto \textbf{ret}\ (\textbf{ms}\ v_2) \gg\!= \lambda x.\ \textbf{send}\ \mathsf{p}_1\ x \gg\!= \lambda x.\ \textbf{ret}\ (\text{br}_2\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\hline
\mathsf{p}_1 \mapsto \textbf{ret}\ (\text{mrg}\ (\iota_2\ (\textbf{ms}\ v_1, \textbf{ms}\ v_2))) = \textbf{ret}\ (\text{mrg}\ ((\text{id} + \textbf{ms} \times \textbf{ms})(\text{spl}\ v))) = \textbf{ret}\ (\textbf{ms}\ v)\\
\mathsf{p}_2 \mapsto \textbf{ret}\ () \mid \mathsf{p}_3 \mapsto \textbf{ret}\ ()
\end{array}
$$

Figure 13. Step-by-step execution of the parallel code for **ms**. Input is $[\mathsf{p}_1 \mapsto v]$, with $\text{spl}\ v = \iota_2\ (v_1, v_2)$.

p_1	p_2	p_3	
$\lambda x.\ \textbf{ret}\ x$	$\lambda x.\ \textbf{ret}\ x$	$\lambda x.\ \textbf{ret}\ x$	$[\![\text{spl@p}_1]\!]\ (\mathsf{Ls@p}_1)$
$\lambda x.\ \textbf{ret}\ (\text{spl}\ x)$	$\lambda x.\ \textbf{ret}\ x$	$\lambda x.\ \textbf{ret}\ x$	$[\![[\mathsf{p}_1 \oplus \mathsf{p}_1\mathsf{p}_2\mathsf{p}_3]]\!]\ (((1 + a) + \mathsf{Ls} \times \mathsf{Ls})@\mathsf{p}_1)$
$\lambda x.\ \textbf{ret}\ (\text{spl}\ x) \gg\!= \lambda x.\textbf{sel}\ \{\mathsf{p}_2, \mathsf{p}_3\}$ $(\lambda x.\ \textbf{ret}\ x)\ (\lambda x.\ \textbf{ret}\ x)$	$\lambda x.\ \textbf{brn}\ \{\mathsf{p}_1\}$ $(\lambda x.\ \textbf{ret}\ x)\ (\lambda x.\ \textbf{ret}\ x)$	$\lambda x.\ \textbf{brn}\ \{\mathsf{p}_1\}$ $(\lambda x.\ \textbf{ret}\ x)\ (\lambda x.\ \textbf{ret}\ x)$	$[\![\text{id} + \ldots]\!]\ (((1 + a) + \mathsf{Ls} \times \mathsf{Ls})$ $@(\iota_1\ \mathsf{p}_1 \cup^{\mathsf{p}_1\mathsf{p}_2\mathsf{p}_3} \iota_2\ \mathsf{p}_2))$

Figure 14. Example Translation

p_1	p_2	p_3	
$\lambda x.\ \textbf{ret}\ (\pi_1\ x) \gg\!= \lambda y.\ \textbf{send}\ \mathsf{p}_2\ y \gg\!=$ $\lambda z.\ \textbf{ret}\ (\pi_2\ x) \gg\!= \lambda t.\ \textbf{send}\ \mathsf{p}_3\ z \gg\!=$ $\lambda r.\ \textbf{ret}(z, r)$	$\lambda\_.\textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!=$ $\lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\ \textbf{ret}\ (x, ())$	$\lambda\_.\textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!=$ $\lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\ \textbf{ret}\ ((), x)$	$[\![(\textbf{ms@p}_2 \circ \pi_1@\mathsf{p}_1)\triangle$ $(\textbf{ms@p}_3 \circ \pi_2@\mathsf{p}_1)]\!]((\mathsf{Ls} \times \mathsf{Ls})@\mathsf{p}_1)$

Figure 15. Right branch for mergesort.

$$
\begin{array}{l}
\mathsf{p}_1 \mapsto \lambda x.\ \textbf{sel}\ \{\mathsf{p}_2, \mathsf{p}_3\}\ (\text{spl}\ x)\ (\lambda x.\ \textbf{ret}\ (\text{mrg}\ (\iota_1\ x)))\\
\qquad (\lambda x.\ \textbf{send}\ \mathsf{p}_2\ (\pi_1\ x) \gg\!= \lambda y.\ \textbf{send}\ \mathsf{p}_3\ (\pi_2\ x) \gg\!= \lambda\_.\\
\qquad\quad \textbf{recv}\ \mathsf{p}_2\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{recv}\ \mathsf{p}_3\ \mathsf{Ls} \gg\!= \lambda y.\ \textbf{ret}\ (\text{mrg}\ (\iota_2\ (x, y))))\\
\qquad \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_2 \mapsto \lambda x.\ \textbf{brn}\ \mathsf{p}_1\ (\textbf{ret}\ x)\ (\textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\\
\qquad \textbf{send}\ \mathsf{p}_1\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)\\
\mathsf{p}_3 \mapsto \lambda x.\ \textbf{brn}\ \mathsf{p}_1\ \ (\textbf{ret}\ x)\ (\textbf{recv}\ \mathsf{p}_1\ \mathsf{Ls} \gg\!= \lambda x.\ \textbf{ret}\ (\textbf{ms}\ x) \gg\!= \lambda x.\\
\qquad \textbf{send}\ \mathsf{p}_1\ x) \gg\!= \lambda x.\ \textbf{ret}\ (\text{join}\ x)
\end{array}
$$

We show in Figure 13 the step-by-step execution of this code on distributed value $[\mathsf{p}_1 \mapsto v]$, with $\text{spl}\ v = \iota_2\ (v_1, v_2)$. The final result is equal to p_1 applying **ms** directly on the input.

Typing Mp against Local Types We define a relation between Mp code and the local type that captures their communication behaviour (Fig. 16). We define a judgement of the form $\Gamma \vdash m : \mathsf{Mp}\ L\ a$, where $\mathsf{Mp}\ L\ a$ is the type of an Mp expression that conforms to protocol L and returns a value of type a. The types are parameterised by a variable l that represents a local type continuation. The rules in Fig. 16 are straightforward, since they relate in a one-to-one way to the constructs of local types.

Semantics The operational semantics of Mp terms is standard, and mirrors that of the local type configurations in [27]. The operational semantics is defined as an LTS with transitions of the form $\langle [\mathsf{p}_i \mapsto m_i]_{i \in I}, W\rangle \rightsquigarrow^{\ell} \langle [\mathsf{p}_i \mapsto m_i']_{i \in I}, W'\rangle$. Here $\ell ::= \mathsf{p}_0\mathsf{p}_1!\langle a\rangle \mid \mathsf{p}_0\mathsf{p}_1?(a) \mid \mathsf{p}_0\mathsf{p}_1 \oplus \iota_i \mid \mathsf{p}_0\mathsf{p}_1\ \&\ \iota_i$ is the observable action that takes place, and represents, respectively, p_0 sends to p_1 a value of type a, p_1 receives from p_0, p_0 sends label i to p_1, and p_1 receives label i from p_0. We use the special symbol ϵ to represent that no communication took place. Finally, W is a mapping from ordered pairs of roles to unbounded buffers that contain the data sent between participants.

Definition A.11. LTS for Mp Terms $\boxed{\langle P, W\rangle \rightsquigarrow^{\ell} \langle P', W'\rangle}$, P, W transitions to P', W' with action ℓ. The transition rules are defined in Fig. 17.

Similarly, we define $\langle C, Q\rangle \rightarrow^{\ell} \langle C', Q'\rangle$ for the LTS of local type configurations (App. A). Here, C is a collection of local types, $C = [\mathsf{p}_i \mapsto L_i]_{i \in I}$, and Q is a mapping from ordered pairs of roles to unbounded buffers that contain *types* of the data exchanged. We also say that W is compatible with Q, $W : Q$, if for all pair $\mathsf{p}_1\mathsf{p}_2$, if $w_1 \cdots w_n = W(\mathsf{p}_1\mathsf{p}_2)$ then $a_1 \cdots a_n = Q(\mathsf{p}_1\mathsf{p}_2)$, and for all i, $w_i : a_i$.

Definition A.12 (Get/Set for Types from One Hole Contexts)**.** Whenever a role performs a choice, we have a type

$$\textsc{Ret}\ \frac{\Gamma \vdash v : a}{\Gamma \vdash \mathbf{ret}\ v : \mathsf{Mp}\ \mathsf{end}\ a} \qquad \textsc{Send}\ \frac{\Gamma \vdash v : a}{\Gamma \vdash \mathbf{send}\ r\ v : \mathsf{Mp}\ (\mathsf{p}!\langle a\rangle.\ \mathsf{end})\ 1}$$

$$\textsc{Recv}\ \frac{}{\Gamma \vdash \mathbf{recv}\ r\ a : \mathsf{Mp}\ (\mathsf{p}?(a).\ \mathsf{end})\ a} \qquad \textsc{Abs}\ \frac{\Gamma, x : a \vdash m : \mathsf{Mp}\ L\ b}{\Gamma \vdash \lambda x.m : a \to \mathsf{Mp}\ L\ b}$$

$$\textsc{Bind}\ \frac{\Gamma \vdash m : \mathsf{Mp}\ L_1\ a \quad \Gamma \vdash f : a \to \mathsf{Mp}\ L_2\ b}{\Gamma \vdash m \gg\!= f : \forall l_2.\ \mathsf{Mp}\ (L_1 \fatsemi L_2)\ a}$$

$$\textsc{Branch}\ \frac{\Gamma \vdash m_1 : \mathsf{Mp}\ L_1\ a_1 \quad \Gamma \vdash m_2 : \mathsf{Mp}\ L_2\ a_2}{\Gamma \vdash \mathbf{brn}\ r\ m_1\ m_2 : \mathsf{Mp}\ (r\ \&\ \{\iota_1.L_1;\ \iota_2.L_2\})\ (a_1 \uplus a_2)}$$

$$\textsc{Select}\ \frac{\Gamma \vdash v : a + b \qquad \Gamma \vdash f_1 : a \to \mathsf{Mp}\ L_1\ c_1 \qquad \Gamma \vdash f_2 : b \to \mathsf{Mp}\ L_2\ c_2}{\Gamma \vdash \mathbf{sel}\ v\ \{r_j\}_{j\in J}\ f_1\ f_2 : \mathsf{Mp}\ (\{\mathsf{p}_j\}_{j\in J} \oplus \{\iota_1.L_1;\ \iota_2.L_2\})\ (c_1 \uplus c_2)}$$

$$\textsc{Case}\ \frac{\Gamma \vdash f_1 : a_1 \to \mathsf{Mp}\ L_1\ b_1 \quad \Gamma \vdash f_2 : a_2 \to \mathsf{Mp}\ L_2\ b_2}{\Gamma \vdash \mathbf{case}\ f_1\ f_2 : a_1 \uplus a_2 \to \mathsf{Mp}\ (L_1 \cup L_2)\ (b_1 \uplus b_2)}$$

Figure 16. Typing rules for Mp code.

with one hole, $a@\mathcal{I}[\mathsf{p}]$, with a sum-type at the hole $(b + c)@\mathsf{p}$. The code for p requires to extract the sum type from the type a, and to set the value at the hole pointed by $\mathcal{I}$. This is because p may occur deep in $\mathcal{I}[\mathsf{p}]$ and, therefore $a@\mathcal{I}[\mathsf{p}] \upharpoonright \mathsf{p}$ may be different to $b + c$. We achieve this with the following families of functions, $\mathsf{get}_{\mathcal{I}} : a \to \mathsf{typeAt}(\mathcal{I}, a)$, and $\mathsf{put}_{\mathcal{I}} : c \times a \to \mathsf{substTy}(\mathcal{I}, a, c)$, where typeAt and substTy get/set the type at the hole in $\mathcal{I}$.

B Deadlock-Freedom

B.1 Proof of Lemma 4.2

Lemma 4.2. *[Existence of Associated Global Type] For all* $\mathsf{WF}(A)$*, if* $\vdash \mathsf{e} : A \to B$*, then there exists* G *s.t.* $\vDash \mathsf{e} \Leftarrow A \sim G$.

Proof. By induction on the structure of the derivation $\vdash \mathsf{e} : A \to B$.

Case Join. $\vdash e \Rightarrow \mathsf{e} : A \cup^{\vec{\mathsf{p}}} A \to B \cup^{\vec{\mathsf{p}}} B$. By the IH, $\vDash \mathsf{e} \Leftarrow A \sim G$. By rule Alt, $\vDash \mathsf{e} \Leftarrow A \cup^{\vec{\mathsf{p}}} A \sim G \cup G$.

Case Alg. $\vdash e \Rightarrow e@\mathsf{p} : a@I \to b@\mathsf{p}$. By Alg, $\vDash e@r \Leftarrow a@I \sim [a@I \rightsquigarrow r]$.

Case $\textsc{Inj}_i$. By $\textsc{Inj}_i$, $\vDash \iota_i \Leftarrow A \sim \mathsf{end}$.

Case Alt. $\vdash e \Rightarrow \mathsf{e} : A_1 \cup^{\vec{\mathsf{p}}} A_2 \to B_1 \cup^{\vec{\mathsf{p}}} B_2$, with $\mathsf{pids}(\mathsf{e}) \subseteq \vec{\mathsf{p}}$. By the IH, $\vDash \mathsf{e} \Leftarrow A_1 \sim G_1$ and $\vDash \mathsf{e} \Leftarrow A_2 \sim G_2$. By Alt, $\vDash \mathsf{e} \Leftarrow A_1 \cup A_2 \sim G_1 \cup^{\vec{\mathsf{p}}} G_2$.

Case Id. By $\vDash \mathsf{id} \Leftarrow a@I \sim \mathsf{end}$.

Case Choice. $\vdash e \Rightarrow [\mathsf{p} \oplus \vec{\mathsf{p}}] : a@\mathcal{I}[\mathsf{p}] \to a@(\mathcal{I}[\iota_1\ \mathsf{p}] \cup^{\vec{\mathsf{p}}} \mathcal{I}[\iota_2\ \mathsf{p}])$. By rule Choice, $\vDash [\mathsf{p} \oplus \vec{\mathsf{p}}] \Leftarrow a@\mathcal{I}[\mathsf{p}] \sim \mathsf{p} \to \{\vec{\mathsf{p}}\}\ \{\iota_1.\ \mathsf{end};\ \iota_2.\ \mathsf{end}\}$.

Case $\textsc{Proj}_i$. By rule $\textsc{Proj}_i$, $\vDash \pi_i \Leftarrow A_1 \times A_2 \sim \mathsf{end}$.

Case Comp. $\vdash e_1 \circ e_2 \Rightarrow \mathsf{e}_1 \circ \mathsf{e}_2 : A \to C$. This implies that $\vdash e_1 \Rightarrow \mathsf{e}_1 : B \to C$ and $\vdash e_2 \Rightarrow \mathsf{e}_2 : A \to B$. By the IH, $\vDash \mathsf{e}_2 \Leftarrow A \sim G_2$, and $\vDash \mathsf{e}_1 \Leftarrow B \sim G_1$. We proceed by induction on the size of B. The base case is $b@I_2$. In this case, A must be $a@I_1$, so G_1 must not be $\cup$ or contain any choices. Therefore, $G_1 \fatsemi G_2$ is defined, and equal to $[G_2/\mathsf{end}]G_1$. If $B = b@(R_{21} \cup R_{22})$, then $G_2 = G_{21} \cup G_{22}$. There are two cases: (1) $G_1 = G_{11} \cup G_{12}$, or (2) there is a choice in G_1, i.e. $G_1 = [\mathsf{p} \to \vec{\mathsf{p}}\{\iota_1.\ G_{11}; \iota_2.\ G_{12}\}/\mathsf{end}]G_1'$. In the first case, we have that $\vDash \mathsf{e}_1 \Leftarrow a@R_{1i} \sim G_{1i}$, and $\vDash \mathsf{e}_2 \Leftarrow a@R_{2i} \sim G_{2i}$, which by the IH implies that $G_{1i} \fatsemi G_{2i}$ is defined. Therefore, $(G_{11} \cup G_{12}) \fatsemi (G_{21} \cup G_{22}) = (G_{11} \fatsemi G_{21}) \cup (G_{12} \fatsemi G_{22})$. In the second case, there must be two sub-expressions of e_1, e_{11} and e_{12} s.t. $\mathsf{e}_{11} \Leftarrow a@I \sim G_1'$, and $\mathsf{e}_{12} \circ [\mathsf{p} \oplus \vec{\mathsf{p}}] \Leftarrow d@\mathcal{I}[\mathsf{p}] \sim \mathsf{p} \to \vec{\mathsf{p}}\{\iota_1.\ G_{11}; \iota_2.\ G_{12}\}$, with $\mathsf{e}_{12} : d@\mathcal{I}[\iota_i\ \mathsf{p}] \to b@R_{2i}$ and $\mathsf{e}_{12} \Leftarrow d@\mathcal{I}[\iota_i\ \mathsf{p}] \sim G_{1i}$. By the IH, $G_{1i} \fatsemi G_{2i}$ must be defined, which implies that $\mathsf{p} \to \vec{\mathsf{p}}\{\iota_1.\ G_{11}; \iota_2.\ G_{12}\} \fatsemi (G_{21} \cup G_{22})$ is defined, and therefore $G_1 \fatsemi (G_{21} \cup G_{22})$ is also defined.

Case $\textsc{Case}_i$. $\vdash \mathsf{e}_1 \triangledown \mathsf{e}_2 \Rightarrow \mathsf{e}_1 \triangledown \mathsf{e}_2 : \iota_i\ A \to B$. By inversion, $\vdash \mathsf{e}_i \Rightarrow \mathsf{e}_i : A \to B$. By the IH, $\vDash \mathsf{e}_i \Leftarrow A \sim G$. By $\textsc{Case}_i$, $\vDash \mathsf{e}_1 \triangledown \mathsf{e}_2 \Leftarrow \iota_i\ A \sim G$.

Case Split. $\vdash_{\mathsf{det}} \mathsf{e}_1 \triangle \mathsf{e}_2 \Rightarrow \mathsf{e}_1 \triangle \mathsf{e}_2 : a@I \to B \times C$. By inversion, $\vdash \mathsf{e}_1 \Rightarrow \mathsf{e}_1 : a@I \to B$ and $\vdash \mathsf{e}_2 \Rightarrow \mathsf{e}_2 : a@I \to C$. By the IH, $\vDash \mathsf{e}_1 \Leftarrow a@I \sim G_1$ $\vDash \mathsf{e}_2 \Leftarrow a@I \sim G_2$. Therefore, $\vDash \mathsf{e}_1 \triangle \mathsf{e}_2 \Leftarrow a@I \sim [G_2/\mathsf{end}]G_1$. □

B.2 Proof of Lemma 4.3

Lemma 4.3. *[Protocol Deadlock-Freedom] For all* $\mathsf{WF}(A)$*, if* $\vdash \mathsf{e} : A \to B$ *and* $\vDash \mathsf{e} \Leftarrow A \sim G$*, then* $\mathsf{WF}(G)$.

Proof. By induction on the structure of the derivation $\vdash \mathsf{e} : a@I \to B$.

Case Join. $\vdash e \Rightarrow \mathsf{e} : A \cup^{\vec{\mathsf{p}}} A \to B \cup^{\vec{\mathsf{p}}} B$. By case analysis, the only possibility for G is that it is obtained via the Join protocol rule: $\vDash \mathsf{e} \Leftarrow A \cup A \sim G$. By the Join typing rule, $\vdash e \Rightarrow \mathsf{e} : A \to B$. By the Join protocol rule. $\vDash \mathsf{e} \Leftarrow A \cup^{\vec{\mathsf{p}}} A \sim G \cup G$. By the IH, $\mathsf{WF}(G)$, therefore $\mathsf{WF}(G \cup G)$.

Case Alg. $\vdash e \Rightarrow e@\mathsf{p} : a@I \to b@\mathsf{p}$. By case analysis, $\vDash e@\mathsf{p} \Leftarrow a@I \sim [a@I \rightsquigarrow \mathsf{p}]$. By definition, $\mathsf{WF}([a@I \rightsquigarrow \mathsf{p}])$.

Case $\textsc{Inj}_i$. $\vDash \iota_i \Leftarrow a@I \sim \mathsf{end}$. Trivial $\mathsf{WF}(\mathsf{end})$.

Case Alt. $\vdash e \Rightarrow \mathsf{e} : A_1 \cup^{\vec{\mathsf{p}}} A_2 \to B_1 \cup^{\vec{\mathsf{p}}} B_2$, with $\mathit{getRoles}(\mathsf{e}) \subseteq \vec{\mathsf{p}}$, $\vdash e \Rightarrow \mathsf{e} : A_1 \to B_1$, $\vdash e \Rightarrow \mathsf{e} : A_2 \to B_2$, and $A_1 \neq A_2$. We know, by a straightforward induction on the typing rules for PAlg, that if $\vdash \mathsf{e} : A_1 \cup^{\vec{\mathsf{p}}} A_2 \to B$, and $A_1 \neq A_2$, then $\mathsf{pids}(A_i) \subseteq \vec{\mathsf{p}}$. By the Alt protocol rule, $\vDash \mathsf{e} \Leftarrow A_1 \cup^{\vec{\mathsf{p}}} A_2 \sim G_1 \cup^{\vec{\mathsf{p}}} G_2$, with $\vDash \mathsf{e} \Leftarrow A_1 \sim G_1$ $\vDash \mathsf{e} \Leftarrow A_2 \sim G_2$. By the IH, $\mathsf{WF}(G_1)$ and

$$\boxed{\langle P, W\rangle \rightsquigarrow^{\ell} \langle P', W'\rangle} \quad P = [\mathsf{p}_i \mapsto m_i]_{i\in I} \quad W = [\mathsf{p}_i\mathsf{p}_j \mapsto w]_{i\in I, j\in I}$$

$$\frac{\langle P[\mathsf{p} \mapsto m],\ W\rangle \rightsquigarrow^{\ell} \langle P[\mathsf{p} \mapsto m'],\ W'\rangle}{\langle P[\mathsf{p} \mapsto m \gg\!= f],\ W\rangle \rightsquigarrow^{\ell} \langle P[\mathsf{p} \mapsto m' \gg\!= f],\ W'\rangle} \qquad \langle P[\mathsf{p} \mapsto \mathbf{ret}\ v \gg\!= f],\ W\rangle \rightsquigarrow^{\epsilon} \langle P[\mathsf{p} \mapsto f\ v],\ W\rangle$$

$$\langle P[\mathsf{p}_1 \mapsto \mathbf{send}\ \mathsf{p}_2\ (v : a)],\ W[\mathsf{p}_1\mathsf{p}_2 \mapsto w]\rangle \rightsquigarrow^{\mathsf{p}_1\mathsf{p}_2!\langle a\rangle} \langle P[\mathsf{p}_1 \mapsto \mathbf{ret}\ ()],\ W[\mathsf{p}_1\mathsf{p}_2 \mapsto v \cdot w]\rangle$$

$$\langle P[\mathsf{p}_1 \mapsto \mathbf{recv}\ \mathsf{p}_2\ a],\ W[\mathsf{p}_2\mathsf{p}_1 \mapsto w \cdot v]\rangle \rightsquigarrow^{\mathsf{p}_2\mathsf{p}_1?(a)} \langle P[\mathsf{p}_1 \mapsto \mathbf{ret}\ v],\ W[\mathsf{p}_2\mathsf{p}_1 \mapsto w]\rangle$$

$$\langle P[\mathsf{p}_0 \mapsto \mathbf{sel}\ (\iota_i\ v)\ \{\}\ f_1\ f_2],\ W\rangle \rightsquigarrow^{\epsilon} \langle P[\mathsf{p}_0 \mapsto f_i\ v \gg\!= \lambda x.\ \mathbf{ret}\ (\mathrm{br}_i\ x)],\ W\rangle$$

$$\langle P[\mathsf{p}_0 \mapsto \mathbf{sel}(\iota_i\ v)\{\mathsf{p}_1 \cdots \mathsf{p}_n\}\ f_1\ f_2], W[\mathsf{p}_0\mathsf{p}_1 \mapsto w]\rangle \rightsquigarrow^{\mathsf{p}_0\mathsf{p}_1 \oplus \iota_i} \langle P[\mathsf{p}_0 \mapsto \mathbf{sel}(\iota_i\ v)\{\mathsf{p}_2 \cdots \mathsf{p}_n\}\ f_1\ f_2], W[\mathsf{p}_0\mathsf{p}_1 \mapsto l_i \cdot w]\rangle$$

$$\langle P[\mathsf{p}_1 \mapsto \mathbf{brn}\ \mathsf{p}_2\ m_1\ m_2],\ W[\mathsf{p}_2\mathsf{p}_1 \mapsto w \cdot l_i]\rangle \rightsquigarrow^{\mathsf{p}_2\mathsf{p}_1 \& \iota_i} \langle P[\mathsf{p}_1 \mapsto m_i \gg\!= \lambda x.\ \mathbf{ret}\ (\mathrm{br}_i\ x)],\ W[\mathsf{p}_2\mathsf{p}_1 \mapsto w]\rangle$$

$$\langle P[\mathsf{p}_1 \mapsto \mathbf{case}\ f_1\ f_2\ (\mathrm{br}_i\ v)],\ W\rangle \rightsquigarrow^{\epsilon} \langle P[\mathsf{p}_1 \mapsto f_i\ v],\ W\rangle$$

Figure 17. Rules for the LTS of Mp terms

$$\mathrm{get}_{[\,]}(x) = x \quad \mathrm{get}_{\mathcal{I}\times I}(x) = \mathrm{get}_{\mathcal{I}}(\pi_1\ x) \quad \mathrm{get}_{I\times\mathcal{I}}(x) = \mathrm{get}_{\mathcal{I}}(\pi_2\ x) \quad \mathrm{get}_{\iota_i\,\mathcal{I}}(x) = \mathrm{get}_{\mathcal{I}}(x)$$
$$\mathrm{put}_{[\,]}(x, y) = x \quad \mathrm{put}_{\mathcal{I}\times I}(x, y) = (\mathrm{put}_{\mathcal{I}}(x, \pi_1\ y), \pi_2\ y) \quad \mathrm{put}_{I\times\mathcal{I}}(x, y) = (\pi_1\ y, \mathrm{put}_{\mathcal{I}}(x, \pi_2\ y)) \quad \mathrm{put}_{\iota_i\ \mathcal{I}}(x, y) = \mathrm{put}_{\mathcal{I}}(x, y)$$

Figure 18. Get/Set for Types from One Hole Contexts

$\mathsf{WF}(G_2)$. Since $\mathsf{pids}(G_i) \subseteq \mathsf{pids}(p) \cup \mathsf{pids}(A_1) \cup \mathsf{pids}(A_2) \subseteq \mathsf{pids}(r)$, $\mathsf{WF}(G_1 \cup^{\vec{\mathsf{p}}} G_2)$.

Case Id. Trivial by WF(end).

Case Choice. $\vdash e \Rightarrow \mathsf{e} \circ [\mathsf{p} \oplus \vec{\mathsf{p}}] : a@\mathcal{I}[\mathsf{p}] \to B_1 \cup \vec{\mathsf{p}} B_2$, where $\mathsf{pids}(\mathsf{e}) \subseteq \vec{\mathsf{p}}$, and $\mathcal{I}[\mathsf{p}] \subseteq \vec{\mathsf{p}}$. By the Choice and Alt typing rules, $\vdash e \Rightarrow \mathsf{e} : a@\mathcal{I}[\iota_i\ \mathsf{p}] \to B_i$. By inversion, the protocol rule must be also Choice: $\vDash [\mathsf{p} \oplus \vec{\mathsf{p}}] \Leftarrow a@\mathcal{I}[\mathsf{p}] \sim \mathsf{p} \to \vec{\mathsf{p}}\{\iota_i.\ G_i\}_{i\in[1,2]}$. By the Choice protocol rule, $\vDash \mathsf{e} \Leftarrow a@\mathcal{I}[\iota_i\ \mathsf{p}] \sim G_i$. By the IH, $\mathsf{WF}(G_i)$. Since $\mathsf{pids}(G_i) \subseteq \mathsf{pids}(\mathsf{e}) \cup \mathsf{pids}(\mathcal{I}[\mathsf{p}]) \subseteq \vec{\mathsf{p}}$, then for all $\mathsf{p}' \in G_i$, $(\mathsf{p} \to \vec{\mathsf{p}}\{\iota_i.\ G_i\}_{i\in[1,2]}) \upharpoonright \mathsf{p}'$ must be defined. Therefore, $\mathsf{WF}(\mathsf{p} \to \vec{\mathsf{p}}\{\iota_i.\ G_i\}_{i\in[1,2]})$.

Case Proj$_i$. Trivial by WF(end).

Case Comp. $\vdash e_1 \circ e_2 \Rightarrow \mathsf{e}_1 \circ \mathsf{e}_2 : A \to C$, with $\vdash e_1 \Rightarrow \mathsf{e}_1 : B \to C$ and $\vdash e_2 \Rightarrow \mathsf{e}_2 : A \to B$. By inversion, the only possible protocol rule is also Comp. Therefore, $\vDash \mathsf{e}_1 \circ \mathsf{e}_2 \Leftarrow A \sim G_2 \fatsemi G_1$, with $\vDash \mathsf{e}_2 \Leftarrow A \sim G_2$ and $\vDash \mathsf{e}_1 \Leftarrow B \sim G_1$. By the IH, $\mathsf{WF}(G_1)$ and $\mathsf{WF}(G_2)$. Also, by the induction on the derivation of $\vdash$, we know that $A_1 \cup^{\vec{\mathsf{p}}} A_2$, if $A_1 \neq A_2$, then $\mathsf{pids}(A_i) \subseteq \vec{\mathsf{p}}$. This implies that if G_1 is $G_{11} \cup^{\vec{\mathsf{p}}} G_{12}$, then either the projection onto p of G_{1i} is the same, or $\mathsf{p} \in \vec{\mathsf{p}}$. By the Choice rule, G must be of the form $G'[\mathsf{p} \to \vec{\mathsf{p}}\{\iota_i.G_i\}_{i\in[1,2]}]$, therefore, for for all $\mathsf{p}' \in \mathsf{pids}(G_{1i})$, the projection of $(G_2 \fatsemi (G_{11} \cup G_{12})) \upharpoonright \mathsf{p}'$ must be defined, which implies that $G_2 \fatsemi G_1$ is defined.

Case Case$_i$. $\vdash e_1 \triangledown e_2 \Rightarrow \mathsf{e}_1 \triangledown \mathsf{e}_2 : a@(\iota_i\ I) \to B$ and $\vDash \mathsf{e}_1 \triangledown \mathsf{e}_2 \Leftarrow a@(\iota_i\ I) \sim G$. By the Case$_i$ protocol and typing rules, $\vDash \mathsf{e}_i \Leftarrow A \sim G$ and $\vdash e_i \Rightarrow \mathsf{e}_i : A \to B$. We conclude by the IH that $\mathsf{WF}(G)$.

Case Split. $\vdash e_1 \vartriangle e_2 \Rightarrow \mathsf{e}_1 \vartriangle \mathsf{e}_2 : A \to (b \times c)@(R_1 \times R_2)$ and $\vDash \mathsf{e}_1 \vartriangle \mathsf{e}_2 \Leftarrow A \sim [G_2/\mathsf{end}]G_1$. By the IH, we know that $\mathsf{WF}(G_1)$ and $\mathsf{WF}(G_2)$. By straightforward induction on the structure of G_1, if $\mathsf{WF}(G_i)$, then $\mathsf{WF}([G_2/\mathsf{end}]G_1)$. □

B.3 Proof of Theorem 5.2

Theorem 5.2. *[Protocol Conformance of the Generated Code] If* $\vDash \mathsf{e} \Leftarrow A \sim G$, *then* $[\![\mathsf{e}]\!]\,(A)$ *complies with protocol* G.

Proof. By induction on the structure of the derivation $\vDash \mathsf{e} \Leftarrow A \sim G$.

Case Alt. $\vDash \mathsf{e} \Leftarrow A_1 \cup^{\vec{\mathsf{p}}} A_2 \sim G_1 \cup^{\vec{\mathsf{p}}} G_2$, with $\vDash \mathsf{e} \Leftarrow A_1 \sim G_1$, $\vDash \mathsf{e} \Leftarrow A_2 \sim G_2$. By the IH, $[\![\mathsf{e}]\!]\,(A_i) : (A_i \upharpoonright \mathsf{p}) \to \mathsf{Mp}\ (G_i \upharpoonright \mathsf{p})\ (B_i \upharpoonright \mathsf{p})$. Moreover, we know that if $\mathsf{p} \notin \vec{\mathsf{p}}$, then $[\![\mathsf{e}]\!]\,(A_1)(\mathsf{p}) = [\![\mathsf{e}]\!]\,(A_2)(\mathsf{p})$, and $G_1 \upharpoonright \mathsf{p} = G_2 \upharpoonright \mathsf{p}$. Therefore, by the definition of $\mathsf{E}_1 \uplus \mathsf{E}_2$, $[\![\mathsf{e}]\!]\,(A_1) \uplus^{\vec{\mathsf{p}}} [\![\mathsf{e}]\!]\,(A_2) : \mathsf{Mp}\ (G_1 \cup^{\vec{\mathsf{p}}} G_2)\ (B_1 \cup^{\vec{\mathsf{p}}} B_2)$.

Case Id. $\vDash \mathsf{id} \Leftarrow a@I \sim \mathsf{end}$. By the definition of $[\![\,]\!]$, $[\![\mathsf{id}]\!]\,(a@I) = [] : a@I \to \mathsf{Mp}\ \mathsf{end}\ a@I$.

Case Inj$_i$. $\vDash \iota_i \Leftarrow a@I \sim \mathsf{end}$. By definition, $[\![\iota_i]\!]\,(A) = [] : a@I \to \mathsf{Mp}\ \mathsf{end}\ (a@(\iota_i\ I))$.

Case Alg. $\vDash e@\mathsf{p}_e \Leftarrow a@I \sim [a@I \rightsquigarrow \mathsf{p}_e]$, with $e : a \to b$. We prove by straightforward induction on the structure of I that $f = (\!|a@I \rightsquigarrow \mathsf{p}_e|\!) : a@I \to \mathsf{Mp}\ [a@I \rightsquigarrow \mathsf{p}_e]\ (a@\mathsf{p}_e)$: if $I = \mathsf{p}$, then $[\mathsf{p} \mapsto \lambda x.\ \mathbf{send}\ \mathsf{p}_e\ x, \mathsf{p}_e \mapsto \lambda\_.\ \mathbf{recv}\ \mathsf{p}\ a]$, which clearly follows $[a@\mathsf{p} \rightsquigarrow \mathsf{p}_e]$; if $I = I_1 \times I_2$, then a must be $a_1 \times a_2$, and we have by the IH that $(\!|a_i@I_i \rightsquigarrow \mathsf{p}_e|\!) : \mathsf{Mp}\ [a_i@I_i \rightsquigarrow \mathsf{p}_e]\ (a_i@\mathsf{p}_e)$; and, finally, if $I = \iota_i\ I'$, then $a = a_1 + a_2$, and $(\!|a_i@I \rightsquigarrow \mathsf{p}_e|\!) : \mathsf{Mp}\ [a_i@I \rightsquigarrow \mathsf{p}_e]\ (a_i@\mathsf{p}_e)$, which composed with $[\mathsf{p}_e \mapsto \lambda x.\ \mathbf{ret}\ (\iota_i\ x)]$ has type $\mathsf{Mp}\ [(a_1 + a_2)@(\iota_i\ I) \rightsquigarrow \mathsf{p}_e]\ ((a_1 + a_2)@\iota_i\ \mathsf{p}_e)$.

Case Comp. $\vDash \mathsf{e}_1 \circ \mathsf{e}_2 \Leftarrow A \sim G_2 \fatsemi G_1$. By the Comp rule, $\vDash \mathsf{e}_2 \Leftarrow A \sim G_2$ and $\vDash \mathsf{e}_1 \Leftarrow B \sim G_1$. By the IH, $[\![\mathsf{e}_2]\!]\,(A) : A \to \mathsf{Mp}\ G_2\ B$ and $[\![\mathsf{e}_1]\!]\,(B) : B \to \mathsf{Mp}\ G_1\ C$. Since $G_2 \fatsemi G_1$ is well-formed, then $[\![\mathsf{e}_2]\!]\,(A) \gg\!\!> [\![\mathsf{e}_1]\!]\,(B) : A \to \mathsf{Mp}\ (G_2 \fatsemi G_1)\ C$, since for all p,

$[\![\mathsf{e}_2]\!]\,(A) : A \upharpoonright \mathsf{p} \to \mathsf{Mp}\ G_2\ (B \upharpoonright \mathsf{p})$ and $[\![\mathsf{e}_1]\!]\,(B) : B \upharpoonright \mathsf{p} \to \mathsf{Mp}\ G_1\ (C \upharpoonright \mathsf{p})$, so $[\![\mathsf{e}_2]\!]\,(A) \gg\!= [\![\mathsf{e}_1]\!]\,(B) : A \upharpoonright \mathsf{p} \to \mathsf{Mp}\ (G_1 \fatsemi G_2)\ (C \upharpoonright \mathsf{p})$.

Case Choice.
$\vDash [\mathsf{p} \oplus \vec{\mathsf{p}}] \Leftarrow a@\mathcal{I}[\mathsf{p}_c] \sim \mathsf{p}_c \twoheadrightarrow \{\vec{\mathsf{p}}\}\{\iota_i.\ \mathsf{end}\}_{i\in[1,2]}$.
By the definition of $[\mathsf{p} \oplus \vec{\mathsf{p}}]$,
$\mathsf{p} \mapsto \lambda x.\ \mathbf{sel}\ \{\vec{\mathsf{p}}\}\ (\mathsf{get}_{\mathcal{I}}(x))\ (\lambda y.\ \mathbf{ret}\ (\mathsf{put}_{\mathcal{I}}(y, x)))$
$(\lambda y.\ \mathbf{ret}\ (\mathsf{put}_{\mathcal{I}}(y, x)))$,
which has type $a@\mathcal{I}[\mathsf{p}] \upharpoonright \mathsf{p} \to \mathsf{Mp}\ (\{\vec{\mathsf{p}}\} \oplus \{\iota_i.\ \mathsf{end}\}_{i\in[1,2]})$, and $\mathsf{p}' \in \vec{\mathsf{p}}$, $\mathsf{p}' \mapsto \lambda x.\ \mathbf{brn}\ \mathsf{p}\ (\mathbf{ret}\ x)\ (\mathbf{ret}\ x)$, which has type $a@\mathcal{I}[\mathsf{p}] \upharpoonright \mathsf{p}' \to \mathsf{Mp}\ (\mathsf{p}\ \&\ \{\iota_i.\ \mathsf{end}\}_{i\in[1,2]})$. Therefore $[\mathsf{p} \oplus \vec{\mathsf{p}}] : a@\mathcal{I}[\mathsf{p}_c] \to \mathsf{Mp}\ (\mathsf{p}_c \twoheadrightarrow \{\vec{\mathsf{p}}\}\{\iota_i.\ \mathsf{end}\}_{i\in[1,2]})\ a@(\mathcal{I}[\iota_i\ \mathsf{p}_c] \cup^{\mathsf{p}\vec{\mathsf{p}}} \mathcal{I}[\iota_i\ \mathsf{p}_c])$.

Case $\textsc{Case}_i$. $\vDash \mathsf{e}_1 \triangledown \mathsf{e}_2 \Leftarrow (a_1 + a_2)@(\iota_i\ I) \sim G$. Then, $\vDash \mathsf{e}_i \Leftarrow a_i@I \sim G$. By the IH, $[\![\mathsf{e}_i]\!]\,(a_i@I) : (a_i@I) \to \mathsf{Mp}\ G\ B$. But by the definition of $[\![\ ]\!]$, $[\![\mathsf{e}_1 \triangledown \mathsf{e}_2]\!]\,((a_1+a_2)@(\iota_i\ I)) : ((a_1 + a_2)@(\iota_i\ I)) \to \mathsf{Mp}\ G\ B$.

Case $\textsc{Proj}_i$. $\vDash \pi_i \Leftarrow A_1 \times A_2 \sim \mathsf{end}$. By definition,
$[\![\pi_i]\!]\,(A_1 \times A_2) = [\_ \mapsto \lambda x.\ \mathbf{ret}\ (\pi_i\ x)] : A_1 \times A_2 \to \mathsf{Mp}\ \mathsf{end}\ A_i$.

Case Split. $\vDash \mathsf{e}_1 \triangle \mathsf{e}_2 \Leftarrow A \sim [G_2/\mathsf{end}]G_1$. Then, $\vDash \mathsf{e}_1 \Leftarrow A \sim G_1$, and $\vDash \mathsf{e}_2 \Leftarrow A \sim G_2$. By the IH, $[\![\mathsf{e}_1]\!]\,(A) : A \to \mathsf{Mp}\ G_1\ B$ and $[\![\mathsf{e}_2]\!]\,(A) : A \to \mathsf{Mp}\ G_2\ C$. By definition, $[\![\mathsf{e}_1 \triangle \mathsf{e}_2]\!]\,(A) = [\![\mathsf{e}_1]\!]\,(A) \triangle [\![\mathsf{e}_2]\!]\,(A) :$
$A \to \mathsf{Mp}\ ([G_2/\mathsf{end}]G_1)\ (B \times C)$. □

C Extensionality

Each monadic m represents the code for an individual process. The parallel composition of the set of monadic actions generated from a PAlg expression $\mathsf{e} : A \to B$, each applied to the corresponding value of type $v : A \upharpoonright \mathsf{p}$ represents an execution of the parallel algorithm on an input of type a, if $a@R = A$. Recall from Sec. 5 that the transitions are of the form $\langle P, W\rangle \rightsquigarrow^{\ell} \langle P', W'\rangle$, where P is an environment that contains the code executed by all roles that collaborate to compute the parallel algorithm, and W represents the shared unbounded buffers used by each pair of participants to communicate. We write w for such buffers, where $\emptyset$ is the empty buffer, $v \cdot w$ is the buffer w extended with value v at the leftmost position, and $w \cdot v$ is the buffer w extended with value v at the rightmost position.

Definition C.1 (Type buffers). We write $Q = [\mathsf{p}_i\mathsf{p}_j \to q]_{i\in I, j\in I}$, where q is a buffer of types, that can be either $\emptyset$, $a \cdot q$ or $q \cdot a$. Note that values include singleton types that represent labels: $l_i : l_i$. We say that a buffer $w = v_1 \cdots v_n$ contains types $q = a_1 \cdots a_m$, $w : q$ if: $n = m$ and $v_i : a_i$ for all $i \in [1, n]$. We say that $W : Q$ if for all pairs of roles, $\mathsf{p}_i\mathsf{p}_j$, $W(\mathsf{p}_i\mathsf{p}_j) : Q(\mathsf{p}_i\mathsf{p}_j)$.

Theorem 5.1. *[Soundness] Assume* $\mathsf{E} : \mathsf{Mp}\ C\ A$, $m : \mathsf{Mp}\ L\ a$ *and* $W : Q$. *Suppose* $\langle \mathsf{E}[r \mapsto m], W\rangle \rightsquigarrow^{\ell} \langle \mathsf{E}[r \mapsto m'], W'\rangle$. *Then there exists* $\langle C[r \mapsto L], Q\rangle \to^{\ell} \langle C[r \mapsto L'], Q'\rangle$ *such that* $W' : Q'$ *and* $m' : \mathsf{Mp}\ L'\ a$.

Proof. Straightforward induction on L_i, and case analysis on m_i and the rules $\rightsquigarrow$ and $\to$, since there is a one-to-one correspondence between the rules syntactic constructs in Mp and the local types. For $\rightsquigarrow$ we need to take several ϵ transitions until communication ℓ happens. □

Lemma C.2. *Assume* $G, A, B, X : A$, *and* $f_i : A \upharpoonright \mathsf{p}_i \to \mathsf{Mp}\ (G \upharpoonright \mathsf{p}_i)\ (B \upharpoonright \mathsf{p}_i)$ *for all* $i \in I$. *Let* $P = [\mathsf{p}_i \mapsto f_i]_{i\in I}$ *then there is a unique* Y *s.t.* $P(X) = Y$.

Proof. Straightforward consequence of Lemma 5.1, and Theorem 3.1 in [27]. We know that the traces for G can only differ in the order of the actions, and that this order must preserve the dependencies laid out by G. Therefore, there the result of any possible execution must respect the data dependencies specified by G. □

Lemma C.3. *If* $(P, W) \Downarrow X$ *and* $\langle P, W\rangle \rightsquigarrow \langle P', W'\rangle$, *then* $(P', W') \Downarrow X$.

Theorem 5.3. *[Extensionality] Assume* $e \Rightarrow \mathsf{e} : a@\mathsf{p} \to b@R$ *and* $x : a$ *initially at* p. *If* $e\ x = y$, *then the execution of* $[\![\mathsf{e}]\!]\,(\mathsf{p})$ *also produces* y, *distributed across* R.

Proof. We prove the following generalised statement. Let $e : a \to b$ s.t. $e \Rightarrow \mathsf{e} : A \to B$, $x : a$, and $\vec{i}$ s.t. $\delta^{\vec{i}}_A(x)$ is defined. Then, there is $\vec{j}$, s.t. $[\![\mathsf{e}]\!]\,(A)(\delta^{\vec{i}}_A(x)) = \delta^{\vec{j}}_B([\![e]\!]\ x)$. We define $\delta^{\vec{i}}_A(x) : A$ as follows:

$$
\begin{array}{ll}
\delta^{\epsilon}_I(x) & = \delta_I(x), \\
\delta^{i_1\cdots i_n}_{R_1 \cup^{\vec{\mathsf{p}}} R_2}(x) & = \mathsf{br}^{\vec{\mathsf{p}}}_i\ (\delta^{i_2\cdots i_n}_{R_{i_1}}(x)) \\
\delta_{\mathsf{p}}(x) & = [\mathsf{p} \mapsto x] \\
\delta_{I_1\times I_2}(x, y) & = [\mathsf{p} \mapsto \delta_{I_1}(x)(\mathsf{p}) \times \delta_{I_2}(y)(\mathsf{p})]_{\mathsf{p}\in \mathsf{pids}(I_1\times I_2)} \\
\delta_{\iota_i\ I}(\iota_i\ x) & = \delta_I(x)
\end{array}
$$

We proceed by induction on the structure of the derivation $\vdash e \Rightarrow \mathsf{e} : A \to B$:

- Case Join. We have $\vdash e \Rightarrow \mathsf{e} : A \cup^{\vec{\mathsf{p}}} A \to B \cup^{\vec{\mathsf{p}}} B$ with $\vdash e \Rightarrow \mathsf{e} : A \to B$. By definition, $[\![\mathsf{e}]\!]\,(A \cup^{\vec{\mathsf{p}}} A) = [\![\mathsf{e}]\!]\,(A) \uplus^{\vec{\mathsf{p}}} [\![\mathsf{e}]\!]\,(A)$. We have that $\delta^{i\cdot\vec{i}}_{A\cup^{\vec{\mathsf{p}}}A}(x) = \mathsf{bf}_{i_1}\ (\delta^{\vec{i}}_A(x))$. Then, by the induction hypothesis, there exists $\vec{j}$ s.t.
  $[\![\mathsf{e}]\!]\,(A \cup^{\vec{\mathsf{p}}} A)(\delta^{i\cdot\vec{i}}_{A\cup^{\vec{\mathsf{p}}}A}(x))$
  $= ([\![\mathsf{e}]\!]\,(A) \uplus^{\vec{\mathsf{p}}} [\![\mathsf{e}]\!]\,(A))\ (\mathsf{br}^{\vec{\mathsf{p}}}_i\ \delta^{\vec{i}}_A(x))$
  $= \mathsf{br}^{\vec{\mathsf{p}}}_i\ [\![\mathsf{e}]\!]\,(A)\ (\delta^{\vec{i}}_A(x))$
  $= \mathsf{br}^{\vec{\mathsf{p}}}_i\ (\delta^{\vec{j}}_B([\![e]\!]\ x))$
  $= \delta^{i\cdot\vec{j}}_{B\cup^{\vec{\mathsf{p}}}B}([\![e]\!]\ x)$
- Case Alt. We have $\vdash e \Rightarrow \mathsf{e} : A_1 \cup^{\vec{\mathsf{p}}} A_2 \to B_1 \cup^{\vec{\mathsf{p}}} B_2$ with $\vdash e \Rightarrow \mathsf{e} : A_1 \to B_1$, $\vdash e \Rightarrow \mathsf{e} : A_2 \to B_2$ and $A_1 \neq A_2$. Then, $[\![\mathsf{e}]\!]\,(A_1 \cup^{\vec{\mathsf{p}}} A_2)(\delta^{i\cdot\vec{i}}_{A_1\cup^{\vec{\mathsf{p}}}A_2}(x)) = ([\![\mathsf{e}]\!]\,(A_1) \uplus^{\vec{\mathsf{p}}}$

$[\![\mathsf{e}]\!]\,(A_2))\ (\mathrm{br}_i^{\vec{\mathsf{p}}}\ (\delta^{\vec{i}}_{A_i}(x))) = \mathrm{br}_i^{\vec{\mathsf{p}}}\ [\![\mathsf{e}]\!]\,(A_i)\ (\delta^{\vec{i}}_{A_i}(x))$. Finally, by the induction hypothesis, there exists $\vec{j}$ s.t. $\mathrm{br}_i^{\vec{\mathsf{p}}}\ [\![\mathsf{e}]\!]\,(A_i)(\delta^{\vec{i}}_{A_i}(x)) = \mathrm{br}_i^{\vec{\mathsf{p}}}\ \delta^{\vec{j}}(B_i)([\![e]\!]\ x) = \delta^{i\cdot\vec{j}}(B_1 \cup^{\vec{\mathsf{p}}} B_2)([\![e]\!]\ x)$.

- Case Alg. We have $\vdash e \Rightarrow e@\mathsf{p} : a@I \to b@\mathsf{p}$, with $\vdash e : a \to b$. Then, $[\![e@\mathsf{p}]\!]_{a@I}\,(\delta_I(x)) = [\mathsf{p}_i \mapsto (\!|a@I \rightsquigarrow \mathsf{p}|\!)\ \delta_I(x)$, by straightforward induction on I, there exists a trace $\langle([\mathsf{p}_i \mapsto (\!|a@I \rightsquigarrow \mathsf{p}|\!)(\mathsf{p}_i)] \gg\!= [\mathsf{p} \mapsto \lambda x.\ \mathbf{ret}(e\ x)])\ \delta_I(x), W\rangle \rightsquigarrow^{\ell_1\cdots\ell_m} \langle[\mathsf{p}_i \mapsto \mathbf{ret}\ v_i], W'\rangle$, with $v_i = ()$ for all i s.t. $\mathsf{p}_i \neq \mathsf{p}$, and $v_j = [\![e]\!]\ x$ for $\mathsf{p}_j = \mathsf{p}$. By Theorem 5.2, the only possible interleavings of actions of $[\![e@\mathsf{p}]\!]$ must follow the protocol $[a@I \rightsquigarrow \mathsf{p}]$. Since this implies that send/receive operations must happen respecting the data dependencies, any possible trace must yield the same result.
- Case Inj. $\vdash \iota_i \Rightarrow \iota_i : A \to \iota_i\ A$ straightforward since $[\![\iota_i]\!]\,(A)\ (\delta_A\ (x)) = \delta_{\iota_i\ A}(\iota_i\ x)$.
- Case Id. $\vdash \mathsf{id} \Rightarrow \mathsf{id} : A \to A$ straightforward, since $[\![\mathsf{id}]\!]\,(A)\ (\delta_A(x)) = \delta_A\ (\mathsf{id}\ x)$.
- Case Proj. $\vdash \pi_i \Rightarrow \pi_i : A_1 \times A_2 \to A_i$ straightforward, since $[\![\pi_i]\!]\,(A_1 \times A_2)\ (\delta_{A_1\times A_2}(x)) = \delta_{A_i}(\pi_i\ x)$.
- Case Comp. $\vdash e_1 \circ e_2 \Rightarrow \mathsf{e}_1 \circ \mathsf{e}_2 : A \to C$ with $\vdash e_1 \Rightarrow \mathsf{e}_1 : B \to C$ and $\vdash e_2 \Rightarrow \mathsf{e}_2 : A \to B$. A straightforward consequence of Theorem 5.2 is that if E_1 behaves as G_1 and E_2 as G_2, then $(\mathsf{E}_1 \fatsemi \mathsf{E}_2)(X) = \mathsf{E}_2(\mathsf{E}_1(X))$, since the permutations of actions of $\mathsf{E}_1 \fatsemi \mathsf{E}_2$ must respect $G_1 \fatsemi G_2$. Then, by the definition of $[\![\ ]\!]$, $[\![\mathsf{e}_1 \circ \mathsf{e}_2]\!]\,(A)(\delta^{\vec{i}}_A(x)) = [\![\mathsf{e}_1]\!]\,(B)\ ([\![\mathsf{e}_2]\!]\,(A)\ (\delta^{\vec{i}}_A(x)))$ By the induction hypothesis: $[\![\mathsf{e}_1]\!]\,(B)([\![\mathsf{e}_2]\!]\,(A)(\delta^{\vec{i}}_A(x))) = [\![\mathsf{e}_1]\!]_B\,(\delta^{\vec{j}}_B([\![e_2]\!]\ x)) = \delta^{\vec{k}}_C([\![e_1]\!]([\![e_2]\!]\ x)) = \delta^{\vec{k}}_C([\![e_1 \circ e_2]\!]\ x))$.
- Case Case$_i$. We have $\vdash e_1 \triangledown e_2 \Rightarrow \mathsf{e}_1 \triangledown \mathsf{e}_2 : \iota_i\ A \to B$, with $\vdash e_i \Rightarrow \mathsf{e}_i : A \to B$. Note that $(\delta_{\iota_i\ A}(x))$ is only defined if $x = \iota_i\ x'$. Then, by definition, $[\![\mathsf{e}_1 \triangledown \mathsf{e}_2]\!]\,(\iota_i\ A)\ (\delta_{\iota_i\ A}(\iota_i\ x')) = [\![\mathsf{e}_i]\!]\,(A)\ (\delta_A(x'))$. By the IH, $[\![\mathsf{e}_i]\!]\,(A)\ (\delta_A(x')) = \delta(B)([\![e_i]\!]\ x') = \delta(B)([\![e_1 \triangledown e_2]\!]\ (\iota_i\ x')) = \delta(B)([\![e_1 \triangledown e_2]\!]\ x)$.
- Case Split. We have $\vdash e_1 \vartriangle e_2 \Rightarrow \mathsf{e}_1 \vartriangle \mathsf{e}_2 : A \to B \times C$, with $\vdash e_2 \Rightarrow \mathsf{e}_2 : A \to C$ and $\vdash e_1 \Rightarrow \mathsf{e}_1 : A \to C$. By definition, $[\![\mathsf{e}_1 \vartriangle \mathsf{e}_2]\!]\,(A) = [\![\mathsf{e}_1]\!]\,(A) \vartriangle [\![\mathsf{e}_2]\!]\,(A)$. By Theorem 5.2, we know that this behaves as $G_1 \fatsemi G_2$, if $p_1 \sim G_1$ and $p_2 \sim G_2$. Therefore, we assume again that the interleavings of the subtraces must not affect the data dependencies. Then,

  $([\![\mathsf{e}_1]\!]\,(A)\ (\delta_A(x))) \vartriangle ([\![\mathsf{e}_2]\!]\,(A)\ (\delta_A(x))) = \delta^{\vec{j}}_B([\![\mathsf{e}_1]\!]\ x) \vartriangle \delta^{\vec{k}}_C([\![\mathsf{e}_2]\!]\ x) = \delta^{\vec{j}\cdot\vec{k}}_{B\times C}([\![\mathsf{e}_1 \vartriangle \mathsf{e}_2]\!]\ x)$.
- Case Choice. We have $\vdash e \Rightarrow [\mathsf{p} \oplus \vec{\mathsf{p}}] : a@\mathcal{I}[\mathsf{p}] \to a@(\mathcal{I}[\iota_1\ \mathsf{p}] \cup^{\vec{\mathsf{p}}} \mathcal{I}[\iota_2\ \mathsf{p}])$. We have two cases:
  1. $\mathsf{p} \mapsto \lambda x.\ \mathbf{sel}\ (\mathrm{get}_{\mathcal{I}}(x))\ \{\vec{\mathsf{p}}\}(\lambda y.\mathrm{put}_{\mathcal{I}}(y,x))(\lambda y.\mathrm{put}_{\mathcal{I}}(y,x))$
  2. $\forall \mathsf{p}', \mathsf{p}' \neq \mathsf{p} \wedge \mathsf{p}' \in \vec{\mathsf{p}}, \mathsf{p}' \mapsto \lambda x.\ \mathbf{brn}\ \mathsf{p}\ (\mathbf{ret}\ x)\ (\mathbf{ret}\ x)$

  By case analysis, if $\mathrm{get}_{\mathcal{I}}(x) = \iota_i\ v$, then we have:
  1. $\mathsf{p} \mapsto \mathrm{br}_i\ (\mathrm{put}_{\mathcal{I}}(v,x))$
  2. $\forall \mathsf{p}', \mathsf{p}' \neq \mathsf{p} \wedge \mathsf{p}' \in \vec{\mathsf{p}}, \mathsf{p}' \mapsto \mathbf{ret}\ (\mathrm{br}_i\ x)$

  This is clearly $[\mathsf{p} \oplus \vec{\mathsf{p}}]\ \delta_{\mathcal{I}[\mathsf{p}]}(x) = \mathrm{br}_i^{\vec{\mathsf{p}}}\ \delta_{\mathcal{I}[\iota_i\ \mathsf{p}]}(x) = \delta^i_{\mathcal{I}[\iota_1\ \mathsf{p}]\cup^{\vec{\mathsf{p}}}\mathcal{I}[\iota_2\ \mathsf{p}]}(x)$.

□

D Generated Code

We show now the generated code for mergesort, unrolling the recursive function once.

D.1 Input Alg expression

```
fftTree :: forall n. SINat n -> Int
       -> Tree n (D [Complex Double])
          :=> Tree n (D [Complex Double])
fftTree SZ w
  = lift (intlit SZ &&& (lit w &&& id)
          >>> prim "baseFFT")
fftTree (SS x) w
  = withCDict (cdictTree @(D [Complex Double]) x)
      $
    {- Recursive FFT to EVENS and ODDS -}
    (fftTree x w
     *** fftTree x (w + 2^ toInteger x))
  {- Multiply right side by exponential -}
    >>> id
        *** mapTree x (lit ps2x &&& id >>> mapExp)
            0
    >>>
    {- zipWith add (swap arguments to force butterfly
       pattern
     - &&& zipWith sub
     -}
    zipTree x True lvl w addc
    &&& zipTree x False lvl (w + 2^ toInteger x)
        subc
  where
    lvl :: Int
    lvl = fromInteger (toInteger (SS x) + 1)
    ps2x :: Int
    ps2x = 2 ^ toInteger (SS x)

fft :: SINat n -> (D [Complex Double]) :=> D [
    Complex Double]
fft n =
  withCDict (cdictTree @(D [Complex Double]) n)
    $
  tsplit n deinterleave >>> fftTree n 0 >>> tfold
      n (append @@ 0)
fft5 :: D [Complex Double] :=> D [Complex
    Double]
```

```
fft5 = withSize 5 fft
```

Listing 1. Fragment of FFT.hs

D.2 Main C Code and Atomic Functions

These need to be implemented by the programmer.

```
#include "FFT.h"
#include <inttypes.h>
#include <errno.h>
#include <string.h>
#include <sys/time.h>
#include <stdlib.h>
#include <math.h>

#define REPETITIONS 50

#define BENCHMARKSEQ(s, f) { \
  time = 0; \
  time_diff = 0; \
  time_old = 0; \
  var = 0; \
  for(int i=0; i<REPETITIONS; i++){ \
    in = randvec(s, size); \
    start = get_time(); \
    out = f(in); \
    end = get_time(); \
    free_fftvec(in); \
    time_diff = end - start; \
    time_old = time; \
    time += (time_diff - time)/(i+1); \
    var += (time_diff - time) * (time_diff -
        time_old); \
  } \
  printf("\tK: %d\n", s); \
  printf("\t\tmean: %f\n", time); \
  printf("\t\tstddev: %f\n", REPETITIONS<=1? 0: sqrt(
      var / (REPETITIONS - 1))); \
}

#define WARMUP(f) { \
  for(int i=0; i<REPETITIONS; i++){ \
    in = randvec(0, size); \
    out = f(in); \
    free_fftvec(in); \
  } \
}

double PI = atan2(1, 1) * 4;

int num_stages;
int num_workers;
vec_cplx_t **stages;

vec_cplx_t zip_add(
    pair_pair_int_int_pair_vec_cplx_vec_cplx_t in)
    {
  int lvl = in.fst.fst;
  int wid = in.fst.snd;
  vec_cplx_t l = in.snd.fst;
  vec_cplx_t r = in.snd.snd;
  vec_cplx_t lout = stages[lvl][wid];
  for(int i = 0; i < l.size; i++){
    lout.elems[i] = l.elems[i] + r.elems[i];
  }
  return lout;
}

vec_cplx_t zip_sub(
    pair_pair_int_int_pair_vec_cplx_vec_cplx_t in)
    {
  int lvl = in.fst.fst;
  int wid = in.fst.snd;
  vec_cplx_t l = in.snd.fst;
  vec_cplx_t r = in.snd.snd;
  vec_cplx_t lout = stages[lvl][wid];
  for(int i = 0; i < l.size; i++){
    lout.elems[i] = l.elems[i] - r.elems[i];
  }
  return lout;
}

vec_cplx_t cat(pair_vec_cplx_vec_cplx_t in){
  in.fst.size *= 2;
  return in.fst;
}

void _fft(cplx_t buf[], cplx_t out[], int n, int
    step)
{
  if (step < n) {
    _fft(out, buf, n, step * 2);
    _fft(out + step, buf + step, n, step * 2);

    for (int i = 0; i < n; i += 2 * step) {
      cplx_t t = cexp(-I * PI * i / n) * out[i +
          step];
      buf[i / 2]     = out[i] + t;
      buf[(i + n)/2] = out[i] - t;
    }
  }
}

void show(const char * s, vec_cplx_t in) {
  printf("%s", s);
  for (int i = 0; i < in.size; i++)
    if (!cimag(in.elems[i]))
      printf("%g ", creal(in.elems[i]));
```

```
    else
      printf("(%g, %g) ", creal(in.elems[i]), cimag(
          in.elems[i]));
    printf("\n");
}

void showstep(int stp, const char * s, vec_cplx_t
    in) {
  printf("%s", s);
  for (int i = 0; i < in.size; i+=stp)
    if (!cimag(in.elems[i]))
      printf("%g ", creal(in.elems[i]));
    else
      printf("(%g, %g) ", creal(in.elems[i]), cimag(
          in.elems[i]));
    printf("\n");
}

vec_cplx_t baseFFT(pair_int_pair_int_vec_cplx_t in
    )
{
    int lvl = in.fst;
    int wid = in.snd.fst;
    cplx_t *buf = stages[lvl][wid].elems;
    int n = in.snd.snd.size;

  _fft(buf, in.snd.snd.elems, n, 1);

    return stages[lvl][wid];
}

vec_cplx_t seqfft(vec_cplx_t in)
{
  pair_int_pair_int_vec_cplx_t i = {1, {0, in}};
  return baseFFT(i);
}

vec_cplx_t map_exp(pair_int_pair_int_vec_cplx_t iv
    ){
  int i = iv.snd.fst;
  int ps2x = iv.fst;
  vec_cplx_t in = iv.snd.snd;
  int step = i * in.size;
  for(int k = 0; k < in.size; k++){
    in.elems[k] = in.elems[k] * cexp (2 * - I *
        PI * (k + step) / (ps2x * in.size));
  }
  return in;
}

void free_fftvec(){
  for(int i = 0; i < num_stages; i++){
    free(stages[i][0].elems);
    free(stages[i]);
  }
  free(stages);
}

pair_vec_cplx_vec_cplx_t deinterleave(
    pair_int_int_t iin){
  int wl = iin.fst;
  int wr = iin.snd;

  int mid = stages[0][wl].size/2;

  stages[1][wl].size = mid;
  stages[1][wr].size = mid;
  stages[1][wr].elems = stages[1][wl].elems + mid;

  for(int i = 0; i < stages[0][wl].size; i+= 2){
    stages[1][wl].elems[i/2] = stages[0][wl].elems
        [i];
    stages[1][wr].elems[i/2] = stages[0][wl].elems
        [i+1];
  }
  memcpy(stages[0][wl].elems, stages[1][wl].elems,
      stages[0][wl].size * sizeof(cplx_t));
  stages[0][wr].elems = stages[0][wl].elems + mid;
  stages[0][wr].size = mid;

  for (int i = 2; i < num_stages; i++){
    memcpy(stages[i][wl].elems, stages[1][wl].
        elems, stages[0][wl].size * sizeof(cplx_t))
        ;
    stages[i][wl].size = mid;
    stages[i][wr].elems = stages[i][wl].elems +
        mid;
    stages[i][wr].size = mid;
  }
  stages[0][wl].size = mid;
  return (pair_vec_cplx_vec_cplx_t) { stages[0][wl
      ], stages[0][wr] };
}


vec_cplx_t randvec(int depth, size_t s){
  num_workers = depth <= 1? 1 : 1 << depth - 1;
  num_stages = depth <= 1? 2 : 1 + depth ;
  stages = (vec_cplx_t **)malloc(num_stages *
      sizeof(vec_cplx_t *));
  for (int i = 0; i < num_stages; i++){
    stages[i] = (vec_cplx_t *)malloc(num_workers *
        sizeof(vec_cplx_t));
    stages[i][0].elems = (cplx_t *)calloc(s, sizeof
        (cplx_t));
  }
  stages[0][0].size = s;
```

```
  srand(time(NULL));


  for (int i = 0; i < s; i++) {
    double rand_r = (double)rand() / (double)
        RAND_MAX;
    double rand_i = (double)rand() / (double)
        RAND_MAX;
    stages[0][0].elems[i] = rand_r + rand_i * I;
  }

  for(int j = 1; j < num_stages; j++) {
     memcpy(stages[j][0].elems, stages[0][0].
         elems, s * sizeof(vec_cplx_t));
     stages[j][0].size = s / num_workers;
  }

  for(int i = 0; i < num_stages; i++) {
    for(int j = 1; j < num_workers; j++) {
      stages[i][j] = stages[i][j-1];
    }
  }

  return stages[0][0];
}

void usage(const char *nm){
  printf("Usage: %s <input_size>\n", nm);
  exit(-1);
}

int main(int argc, const char *argv[]) {
  setbuf(stdout, NULL);
  if (argc <= 1) {
    usage(argv[0]);
  }
  char *endptr = NULL;
  errno = 0;
  size_t size = strtoimax(argv[1],&endptr,10);
  size = (size_t) 1 << (long)ceil(log2(size));
  size = size < 256? 256:size;
  if (errno != 0) {
    printf("%s", strerror(errno));
    usage(argv[0]);
  }
  if (endptr != NULL && *endptr != 0) {
    usage(argv[0]);
  }

  vec_cplx_t in, out;
  /* allocate memory */
  in = randvec(size, size);

  /* calling generated fft5 */
  out = fft5(in);
  show("Result: ", out);

  free_fftvec();
}
```

D.3 Automatically Generated C Code

```
#ifndef __FFT__
#define __FFT__

#include<stdio.h>
#include<stdlib.h>
#include<pthread.h>
#include<complex.h>
typedef double _Complex cplx_t;

typedef struct vec_cplx {
          cplx_t * elems; size_t size;
       } vec_cplx_t;

typedef struct q_vec_cplx {
          volatile unsigned int q_size;
          int q_head;
          int q_tail;
          pthread_mutex_t q_mutex;
          pthread_cond_t q_full;
          pthread_cond_t q_empty;
          vec_cplx_t q_mem[1];
       } q_vec_cplx_t;

void q_vec_cplx_put(q_vec_cplx_t *, vec_cplx_t);

vec_cplx_t q_vec_cplx_get(q_vec_cplx_t *);

typedef enum unit {
          Unit
       } unit_t;

typedef struct pair_int_vec_cplx {
          int fst; vec_cplx_t snd;
       } pair_int_vec_cplx_t;

typedef struct pair_int_pair_int_vec_cplx {
          int fst; pair_int_vec_cplx_t snd;
       } pair_int_pair_int_vec_cplx_t;

vec_cplx_t baseFFT(pair_int_pair_int_vec_cplx_t);

vec_cplx_t fft0(vec_cplx_t);

vec_cplx_t fft1(vec_cplx_t);

typedef struct pair_int_int {
```

```
            int fst; int snd;
        } pair_int_int_t;

typedef struct pair_vec_cplx_vec_cplx {
            vec_cplx_t fst; vec_cplx_t snd;
        } pair_vec_cplx_vec_cplx_t;

pair_vec_cplx_vec_cplx_t deinterleave(
    pair_int_int_t);

vec_cplx_t cat(pair_vec_cplx_vec_cplx_t);

typedef struct
    pair_pair_int_int_pair_vec_cplx_vec_cplx {
          pair_int_int_t fst;
              pair_vec_cplx_vec_cplx_t snd;
        }
         pair_pair_int_int_pair_vec_cplx_vec_cplx_t
         ;

vec_cplx_t zip_add(
    pair_pair_int_int_pair_vec_cplx_vec_cplx_t);

vec_cplx_t map_exp(pair_int_pair_int_vec_cplx_t);

vec_cplx_t zip_sub(
    pair_pair_int_int_pair_vec_cplx_vec_cplx_t);

vec_cplx_t fft2(vec_cplx_t);

vec_cplx_t fft3(vec_cplx_t);

vec_cplx_t fft4(vec_cplx_t);

vec_cplx_t fft5(vec_cplx_t);

vec_cplx_t fft6(vec_cplx_t);

vec_cplx_t fft7(vec_cplx_t);

vec_cplx_t fft8(vec_cplx_t);

#endif
```

Listing 2. Generated FFT.h

```
#include "FFT.h"

q_vec_cplx_t ch0 = { 0, 0, 0, { } };
q_vec_cplx_t ch2 = { 0, 0, 0, { } };

q_vec_cplx_t ch3 = { 0, 0, 0, { } };

vec_cplx_t fft2_part_0(vec_cplx_t v_s)
{
    pair_int_int_t v_t;
    v_t.fst = 0;
    v_t.snd = 1;
    pair_vec_cplx_vec_cplx_t v_u;
    v_u = deinterleave(v_t);
    vec_cplx_t v_v;
    v_v = v_u.fst;
    q_vec_cplx_put(&ch0, v_v);
    vec_cplx_t v_w;
    v_w = v_u.snd;
    q_vec_cplx_put(&ch2, v_w);
    vec_cplx_t v_x;
    v_x = q_vec_cplx_get(&ch1);
    vec_cplx_t v_y;
    v_y = q_vec_cplx_get(&ch3);
    pair_vec_cplx_vec_cplx_t v_z;
    v_z.fst = v_x;
    v_z.snd = v_y;
    vec_cplx_t v_aa;
    v_aa = cat(v_z);
    return v_aa;
}

q_vec_cplx_t ch4 = { 0, 0, 0, { } };

q_vec_cplx_t ch5 = { 0, 0, 0, { } };

unit_t fft2_part_1()
{
    vec_cplx_t v_ba;
    v_ba = q_vec_cplx_get(&ch0);
    pair_int_pair_int_vec_cplx_t v_ca;
    v_ca.fst = 1;
    pair_int_vec_cplx_t v_da;
    v_da.fst = 0;
    v_da.snd = v_ba;
    v_ca.snd = v_da;
    vec_cplx_t v_ea;
    v_ea = baseFFT(v_ca);
    q_vec_cplx_put(&ch4, v_ea);
    vec_cplx_t v_fa;
    v_fa = q_vec_cplx_get(&ch5);
    pair_pair_int_int_pair_vec_cplx_vec_cplx_t
        v_ga;
    pair_int_int_t v_ha;
    v_ha.fst = 2;
    v_ha.snd = 0;
    v_ga.fst = v_ha;
    pair_vec_cplx_vec_cplx_t v_ia;
    v_ia.fst = v_ea;
    v_ia.snd = v_fa;
    v_ga.snd = v_ia;
    vec_cplx_t v_ja;
```

```
    v_ja = zip_add(v_ga);
    q_vec_cplx_put(&ch1, v_ja);
    return Unit;
}

unit_t fft2_part_2()
{
    vec_cplx_t v_ka;
    v_ka = q_vec_cplx_get(&ch2);
    pair_int_pair_int_vec_cplx_t v_la;
    v_la.fst = 1;
    pair_int_vec_cplx_t v_ma;
    v_ma.fst = 1;
    v_ma.snd = v_ka;
    v_la.snd = v_ma;
    vec_cplx_t v_na;
    v_na = baseFFT(v_la);
    pair_int_pair_int_vec_cplx_t v_oa;
    v_oa.fst = 2;
    pair_int_vec_cplx_t v_pa;
    v_pa.fst = 0;
    v_pa.snd = v_na;
    v_oa.snd = v_pa;
    vec_cplx_t v_qa;
    v_qa = map_exp(v_oa);
    q_vec_cplx_put(&ch5, v_qa);
    vec_cplx_t v_ra;
    v_ra = q_vec_cplx_get(&ch4);
    pair_pair_int_int_pair_vec_cplx_vec_cplx_t
        v_sa;
    pair_int_int_t v_ta;
    v_ta.fst = 2;
    v_ta.snd = 1;
    v_sa.fst = v_ta;
    pair_vec_cplx_vec_cplx_t v_ua;
    v_ua.fst = v_ra;
    v_ua.snd = v_qa;
    v_sa.snd = v_ua;
    vec_cplx_t v_va;
    v_va = zip_sub(v_sa);
    q_vec_cplx_put(&ch3, v_va);
    return Unit;
}

void * fun_thread_1_1(void * arg)
{
    fft2_part_1();
    return NULL;
}

void * fun_thread_2(void * arg)
{
    fft2_part_2();
    return NULL;
}

vec_cplx_t fft2(vec_cplx_t v_wa)
{
    vec_cplx_t v_xa;
    pthread_t thread1;
    pthread_t thread2;
    pthread_create(&thread1, NULL, fun_thread_1_1,
        NULL);
    pthread_create(&thread2, NULL, fun_thread_2,
        NULL);
    v_xa = fft2_part_0(v_wa);
    pthread_join(thread1, NULL);
    pthread_join(thread2, NULL);
    return v_xa;
}
```

Listing 3. Fragment of generated FFT.c

E Artifact Appendix

E.1 Abstract

This artifact provides a prototype implementation of PAlg, embedded in Haskell, along with a number of benchmarks used to test the scalability of our approach. We provide scripts to regenerate the execution time measurements that we used in our paper. This will allow to evaluate our results on any multi-core shared-memory architecture.

We also provide a small tutorial that is meant to guide a programmer, step-by-step, in the implementation of a message-passing parallel algorithm using our library. The tutorial includes a guide on how to visualise the global types that correspond to the achieved parallelisations, as well as any asynchronous optimisations applicable to the generated message-passing code.

E.2 Artifact check-list (meta-information)

- **Algorithm:** Message-passing C code generation from first-order Haskell functions. Global type inference of the communication protocol followed by the parallelisation.
- **Program:** Haskell libraries `Language.CAlg`, noindent `Language.CAlg.CSyn` and dependencies, as well as `session-arrc`, to compile to C Haskell functions built using such libraries.
- **Compilation:** GHC >= 8.6 && < 8.8, and C compiler that supports C11.
- **Transformations:** Compilation to C, and asynchronous optimisation pass.
- **Binary:** Source code and scripts included to generate the binaries from the sources.
- **Data set:** Included original run-time measurements for comparison.
- **Hardware:** We used a 12-core Intel Xeon CPU E5-2650 v4 @ 2.20GHz. We recommend a shared-memory architecture, with uniform access times, to measure the overheads of our approach, not message latencies.

- **Execution:** We include a script to run the benchmarks.
- **Output:** Benchmark execution times.
- **Experiments:** Small, representative benchmarks of common parallel algorithms.
- **How much memory required (approximately)?:** 64GB for using the maximum benchmark input size.
- **How much time is needed to complete experiments (approximately)?:** 5 days on the hardware stated in §E.3.2.
- **Publicly available?:** Yes.
- **Code licenses (if publicly available)?:** BSD-3.

E.3 Description

E.3.1 How delivered

We provide a docker image with the necessary dependencies: https://imperialcollegelondon.box.com/v/cc20-artifact-p43. After downloading, the image can be loaded using:

```
$ sudo docker load -i cc20-artifact-p43.docker
```

To run the image, run the command:

```
$ sudo docker run -ti cc20-artifact-p43
```

File README.md inside the docker image contains additional instructions. Our benchmarks, source code and scripts are also publicly available on Github, in https://github.com/session-arr/session-arr.

E.3.2 Hardware dependencies

We used a 12-core Intel Xeon CPU E5-2650 v4 @ 2.20GHz. We recommend using a shared-memory architecture, with uniform access times, to measure the overheads of our approach, not message latencies.

E.3.3 Software dependencies

All our dependencies are listed in the `Dockerfile` in our public repository. We list them below. To compile our tool:

1. GHC >= 8.6 (not tested with GHC >= 8.8)
2. *stack* Version 1.9.1

To run our experiments:

1. C compiler that supports C11 (tested with GCC >= 4.8 && < 8.3)
2. glibc (tested with versions >= 2.17 && < 2.29)
3. numactl

To generate the graphs:

1. python (== 2.7)
2. python-matplotlib (== 2)
3. python-pint (== 0.7)

E.3.4 Data sets

We include as part of the artifact the raw data that we obtained for our benchmarks. These are included under `benchmarks/<bench_name>/data/t_<num_cores>`, where `<num_cores>` is either 12 or 24. There is additionally a file `t_48`, that uses all full 24 cores + hyperthreading. The structure of the files is:

```
size: <size>
       K: seq
              mean: <avg_execution_time>
              stddev: <std_dev>
       K: 1
              mean: ...
              stddev: ...
       ...
```

Keyword `size` denotes the size of the inputs for the particular benchmark. Keyword `mean` is the average execution time. Keyword `stddev` is the standard deviation. We write `K:` to denote the number of recursion unfoldings used to produce the parallel version.

Examples of global types for each benchmark are under `benchmarks/<bench_name>/protocol/<bench_name>_<fun_name>.mpst`, where `<fun_name>` is the function name in `<bench_name>.hs` that corresponds to this protocol.

E.4 Installation

Note: this section can be omitted if using our docker image.

We recommend using Stack (https://docs.haskellstack.org/en/stable/README/#how-to-install). To build our tool:

```
$ git clone \
    https://github.com/session-arr/session-arr
$ cd session-arr
$ stack build
```

There is no need to install the tool. However, to install it, run:

```
$ stack install
```

This will copy the binary `session-arrc` to a local directory, usually `${HOME}/.local/bin`.

Manual compilation and installation using GHC is also possible, but we discourage it. Read `session-arr/package.yaml` to find out which haskell packages are required.

E.5 Experiment workflow

E.5.1 Automatic

We included script `session-arr/benchmark.sh` to compile and run all the benchmarks used in the paper. To customise the amount of cores, the number of repetitions per experiment and the maximum input size, run:

```
$ CORES=<ncores> REPETITIONS=<nreps> \
     MAXSIZE=<nsize> ./benchmark.sh
```

The defaults are:

1. `CORES`: number of physical cores on your machine
2. `REPETITIONS`: 50
3. `MAXSIZE`: 30

The script requires that `MAXSIZE` $\geq$ 15.

Note: using `MAXSIZE`= 30 requires a machine with a large amount of memory. We run our experiments on a machine with **64GB** of memory.

E.5.2 Manual

Clone and build the repository:

```
$ git clone \
    https://github.com/session-arr/session-arr
$ cd session-arr
$ stack build
```

Navigate to one of the benchmarks

```
$ cd examples/FFT
```

Here, there should be two files: `FFT.hs` and `main.c`.

```
$ ls
FFT.hs main.c run.sh
```

To run our tool, run `session-arrc` using stack, with the `.hs` file as input.

```
$ stack exec session-arrc -- FFT.hs
```

The tool should output the list of functions found in module `FFT.hs` that are going to be compiled to C, and produce two files `FFT.c` and `FFT.h`. The interface file contains the type definitions and function signatures of the functions in `FFT.c`. Finally, compile `main.c`:

```
$ gcc FFT.c main.c -o bench -lpthread -lm
```

To configure the number of repetitions, recompile the benchmark as follows:

```
$ gcc FFT.c main.c -DREPETITIONS=<num_reps> \
    -o bench -lpthread -lm
```

You may use `run.sh` to run the benchmark on a range of inputs. The usage is:

```
$ ./run.sh <num_cores> <max_size>
```

For example, `./run.sh 2 10` will run the benchmark with sizes 2^9 and 2^{10}. The maximum **size must be** > 9. **To generate the graphs**, you need measurements using at least 7 different sizes, i.e. **size must be** > 14.

Running each benchmark manually Pass a valid input size to bench, the output looks as follows (run in a 4-core machine):

```
$ ./bench $((2**17))
  K: seq
    mean: 0.039446
    stddev: 0.000713
    ...
  K: 4
    mean: 0.011952
    stddev: 0.000636
    ...
```

Save all execution times to files with the format described in §E.3.4, as follows:

```
$ mkdir data
$ echo "size: <size1>" >> data/t_<num_cores>
$ ./bench <size1> >> data/t_<num_cores>
$ ...
$ echo "size: <sizeN>" >> data/t_<num_cores>
$ ./bench <sizeN> >> data/t_<num_cores>
```

Ensure that there are measurements with at least $N > 14$ sizes.

Plotting the speedups: Navigate to `examples/`. The speedups can be plotted using scripts `plotall.sh` and `plot.py`, these will regenerate the graphs used in our paper. The usage is `./plotall.sh BENCHMARK_DIR CORES`, where `CORES` is the number of cores used for the experimental workflow. For example:

```
$ ./plotall.sh FFT 4
```

This will generate the graphs for FFT run on 4 cores under `examples/plots`.

E.6 Evaluation and expected result

If you followed the experiment workflow, you should find under `examples/plots` a series of graphs with the speedups for each benchmark. To visualise them, we recommend copying them to a local directory, by running `docker cp` from **outside** the docker container:

```
$ docker cp \
<NM>:/home/cc20-artifact/session-arr/examples/plots \
<DIR>
```

Here, `<NM>` is the container name obtained via `docker ps -a`, and `<DIR>` is the destination path.

Outcome When run on similar hardware to the one that we describe in the paper, following our workflow, comparable speedups and scalability to the ones that we reported in the paper should be observed.

Note: for more reliable results, execution should be done **outside** the docker container. Use the container to generate all C code, then copy it running `docker cp` from outside the container, as well as the necessary scripts `run.sh`, and proceed locally. If you decide to run the experiments locally, please check §E.3.3 and ensure that you have all required software.

E.7 Experiment customization

Several aspects can be customised in the benchmark source code, execution scripts and compilation options:

- Annotations to functions in the `.hs` files should produce different parallelisations.
- Files `main.c` can be compiled using different numbers of repetitions using `-DREPETITIONS=<num_reps>`.
- The script `benchmark.sh` can be run with such number of repetitions, to reduce execution times. The maximum input size for the benchmarks, and the number of cores can also be customised:

  ```
  $ cd session-arr
  $ REPETITIONS=<num_reps> CORES=<cores> \
      MAXSIZE=<max_size> ./benchmark.sh
  ```

E.8 Methodology

Submission, reviewing and badging methodology:

- http://cTuning.org/ae/submission-20190109.html
- http://cTuning.org/ae/reviewing-20190109.html
- https://www.acm.org/publications/policies/artifact-review-badging